

Silicon planar epitaxial transistors

BF484; BF486; BF488

APPLICATIONS

- Video output stages of black-and-white and colour television receivers.

DESCRIPTION

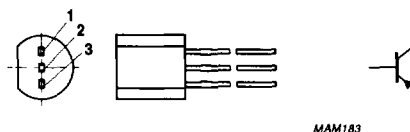
PNP transistors in TO-92 package.

PINNING

PIN	DESCRIPTION
1	base
2	collector
3	emitter

QUICK REFERENCE DATA

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V_{CBO}	collector-base voltage	open emitter			
	BF484		—	—250	V
	BF486		—	—300	V
	BF488		—	—350	V
V_{CEO}	collector-emitter voltage	open base			
	BF484		—	—250	V
	BF486		—	—300	V
	BF488		—	—350	V
I_{CM}	peak collector current		—	—100	mA
P_{Tot}	total power dissipation	free air	—	830	mW
h_{FE}	DC current gain	$I_C = -25$ mA; $V_{CE} = -20$ V	50	—	
f_T	transition frequency	$f = 100$ MHz; $I_C = -10$ mA; $V_{CE} = -10$ V	70	110	MHz
T_j	junction temperature		—	150	°C



MAM183

Fig.1 Simplified outline (TO-92) and symbol.

FOR MORE DETAILED INFORMATION SEE THE LATEST ISSUE OF HANDBOOK SC04 OR DATA SHEET