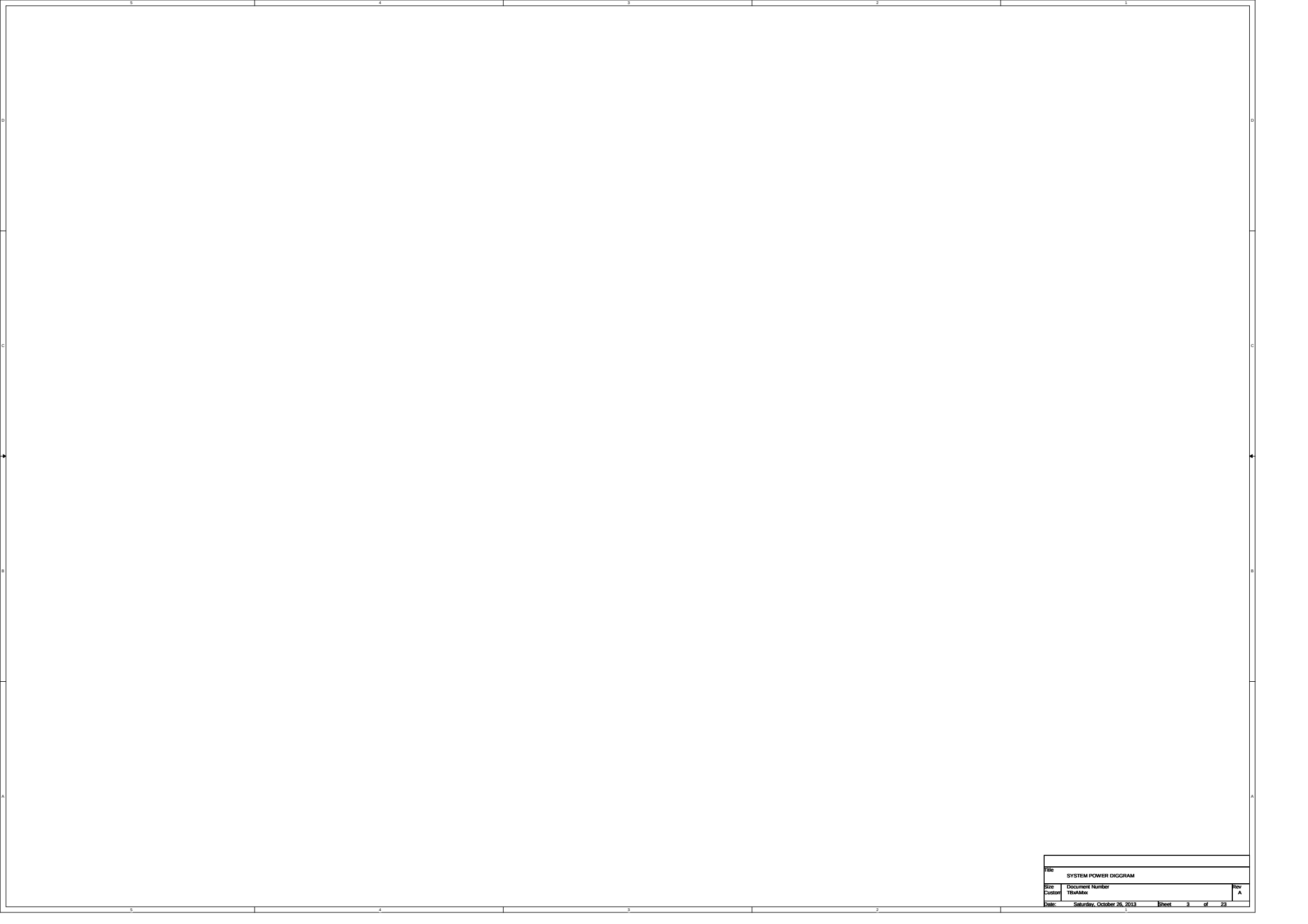


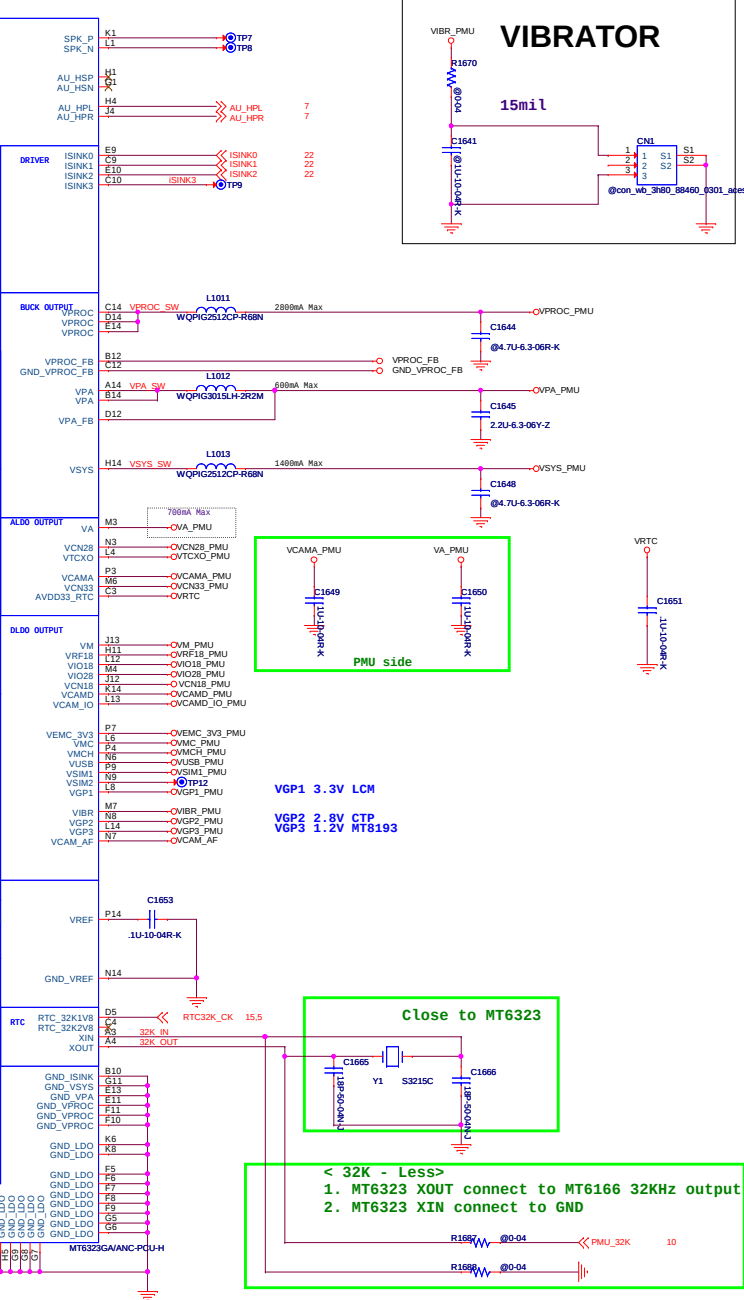
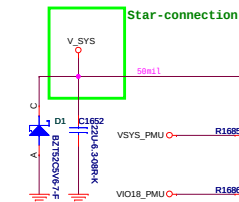
Title			
INDEX			
Size	Document Number		Rev
Custom	TBXAmbox		A
Date:	Saturday, October 26, 2013	Sheet	1 of 23



Title			
BLOCK DIAGRAM			
Size	Document Number		Rev
Custom	TbxAmax		A
Date	Saturday, October 26, 2013	Sheet	2 of 23

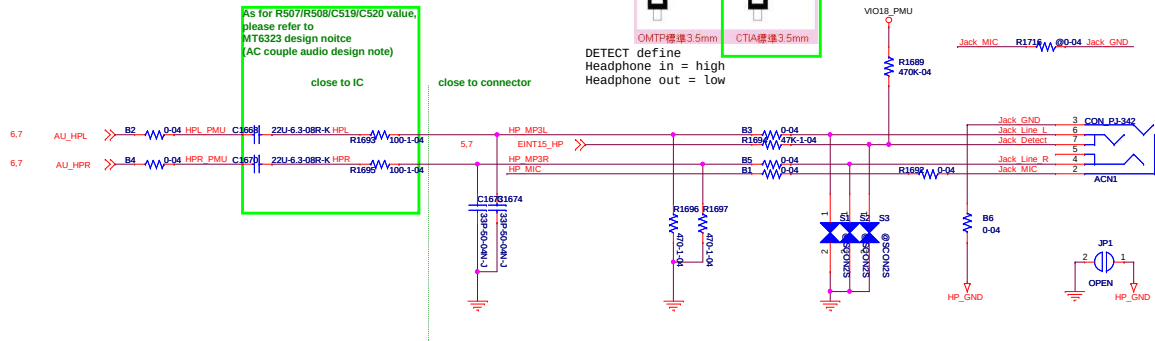


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Size	Document Number		Rev
Custom	TBVAMox		A
Date	Saturday, October 26, 2013	Sheet	3 of 23

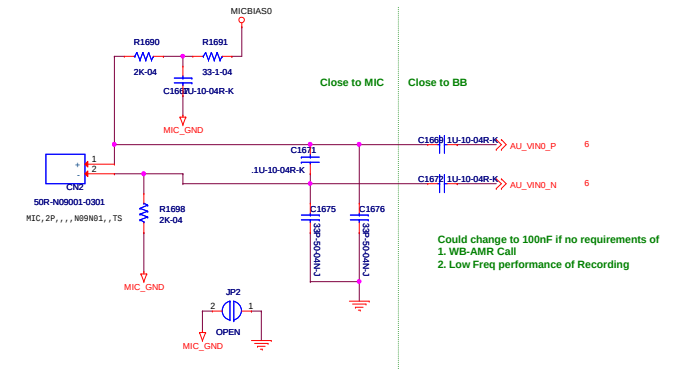


Title			
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Size Custom	Document Number TBxAMxx		Rev A
Date:	Saturday, October 26, 2013	Sheet 6 of 23	

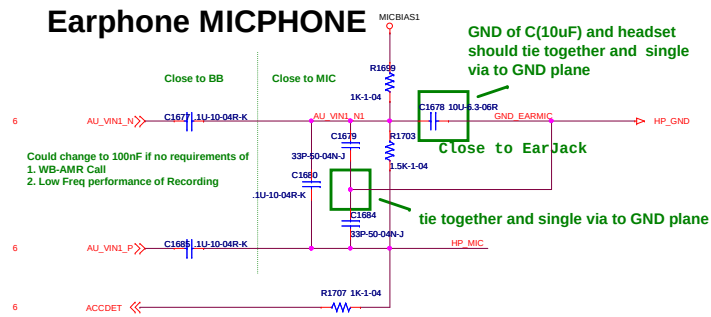
Earphone Audio



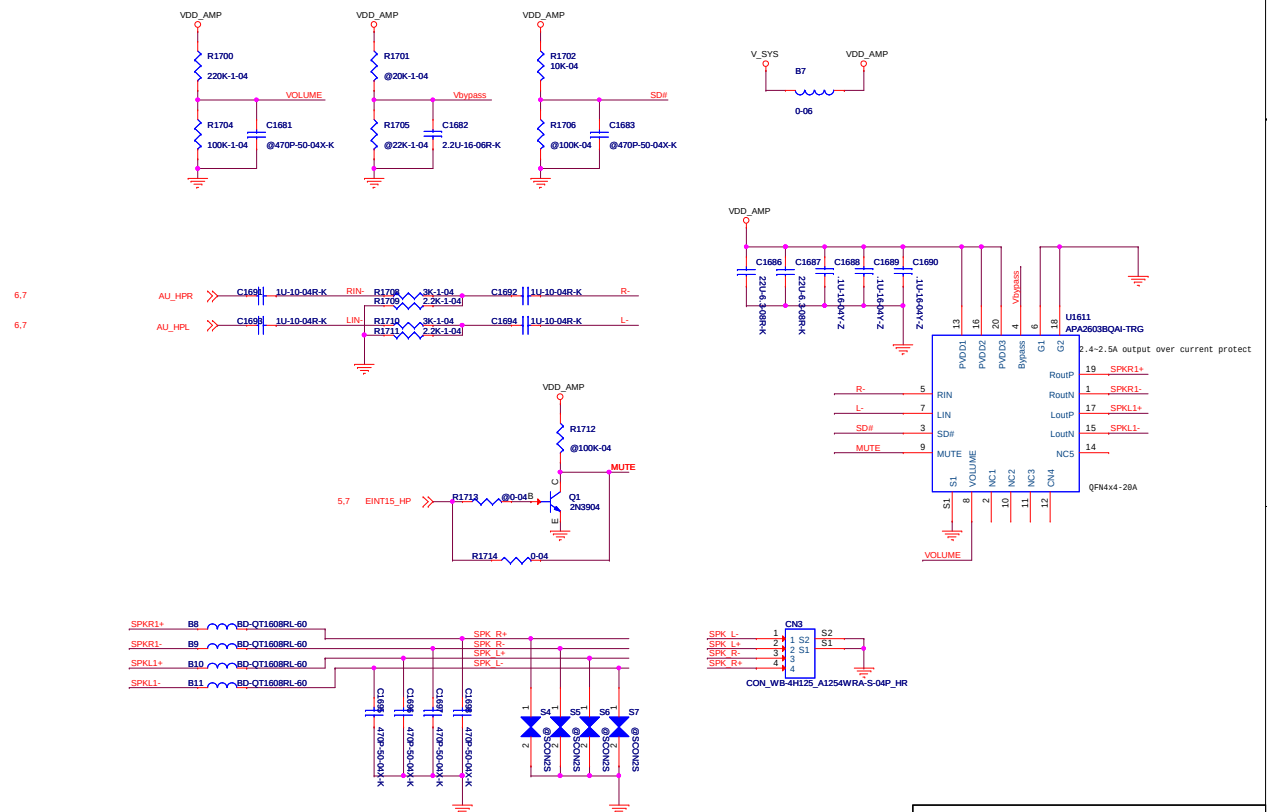
Handset Microphone 1



Earphone MICROPHONE

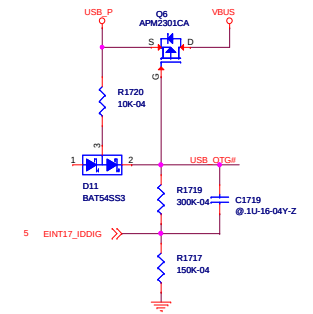
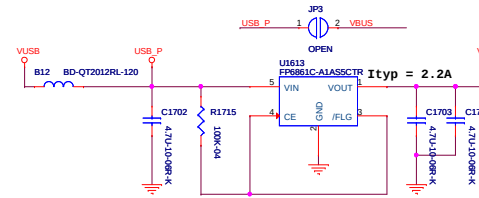
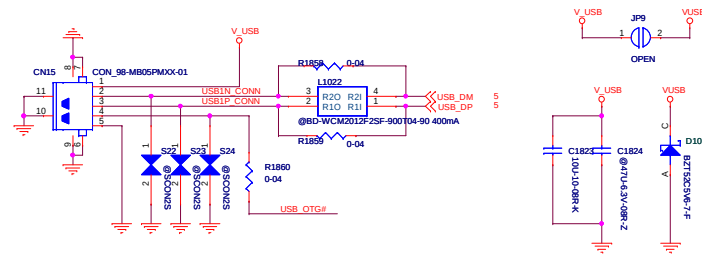


SPEAKER AMP

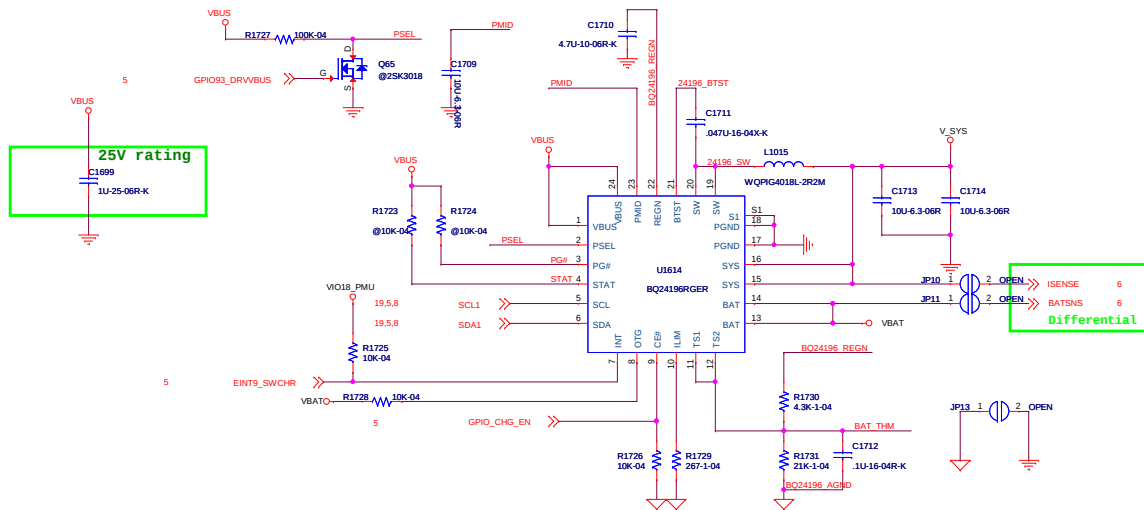


Title AMP/HP/MIC			
Size Custom	Document Number TBxAMbx		Rev A
Date:	Saturday, October 26, 2013	Sheet	7 of 23

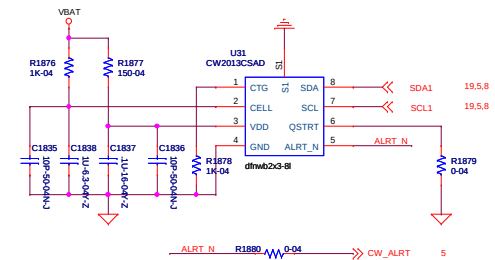
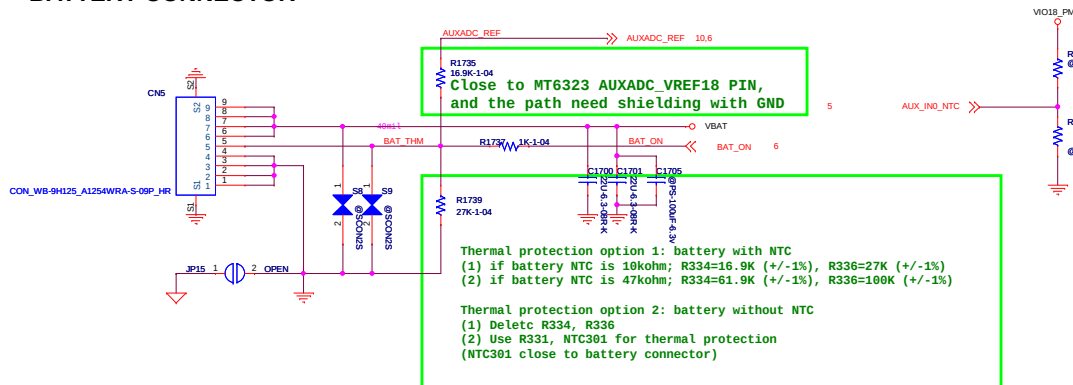
DC IN / Micro USB



Switching Charger

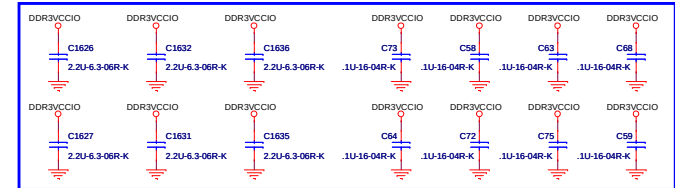
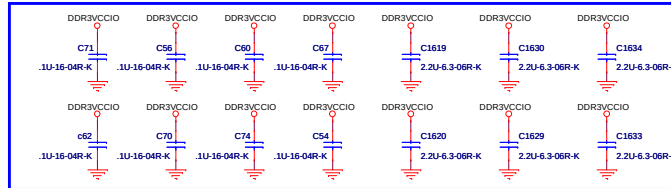
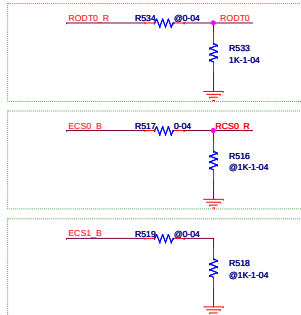
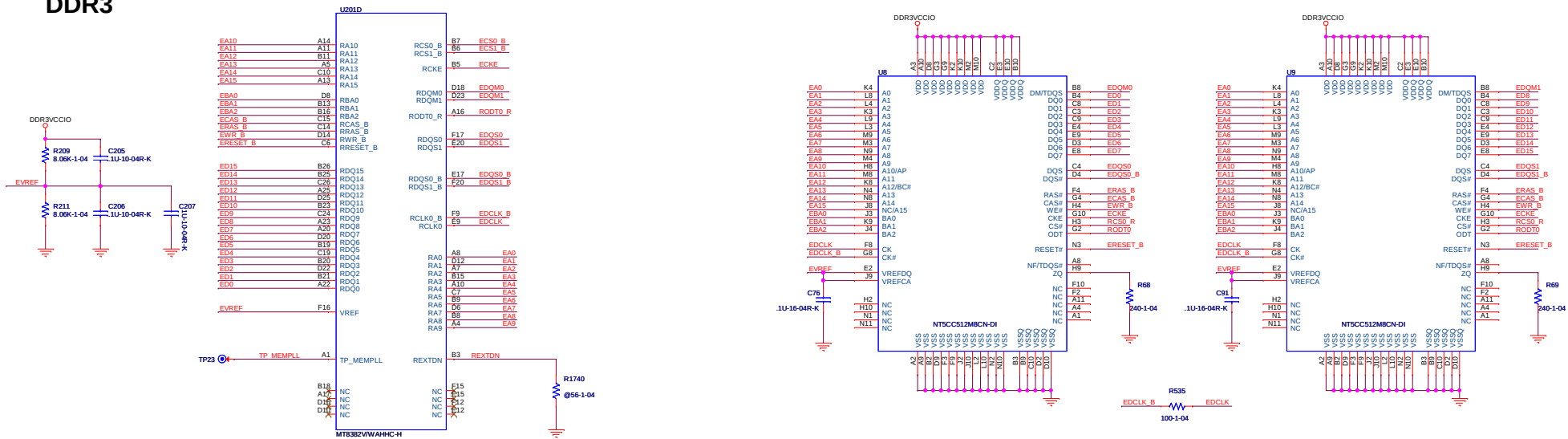


BATTERY CONNECTOR

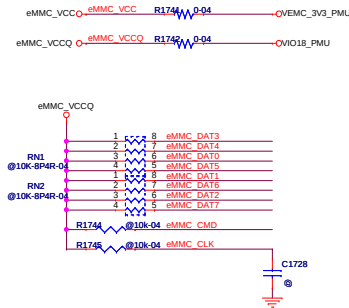
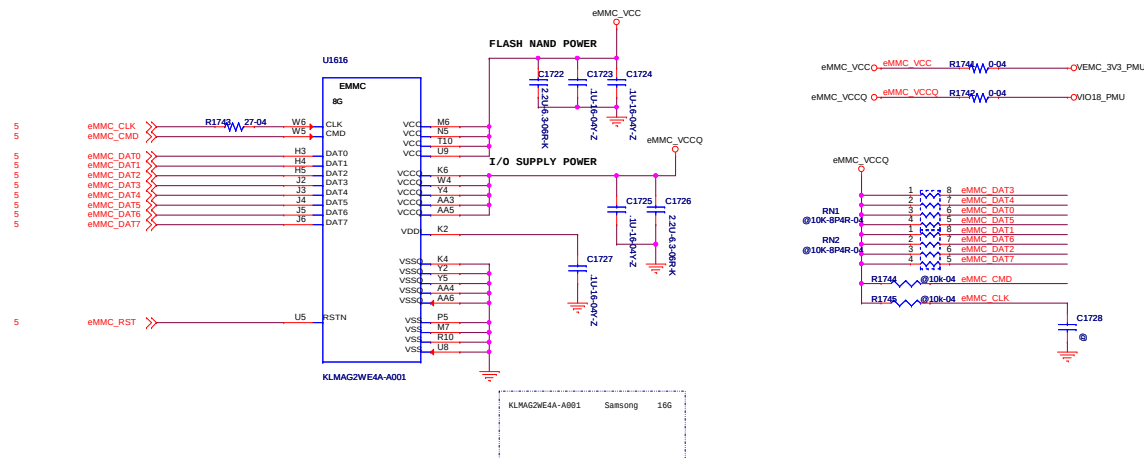


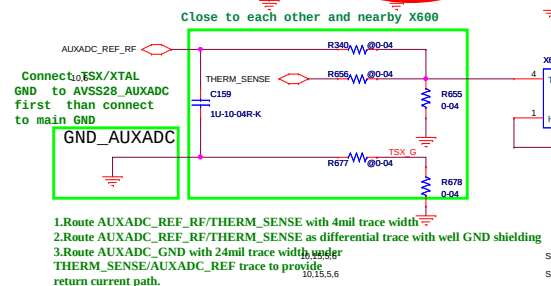
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Size Custom	Document Number TBxAMbx		Rev A
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DDR3



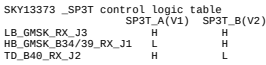
eMMC



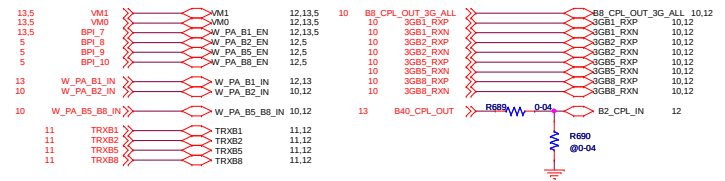
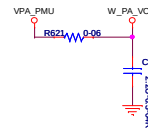
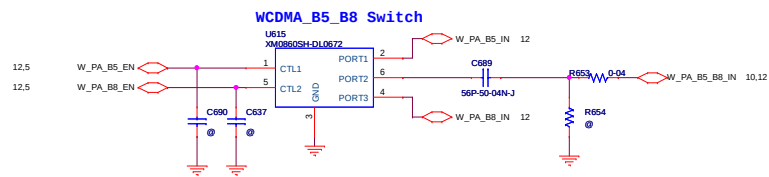
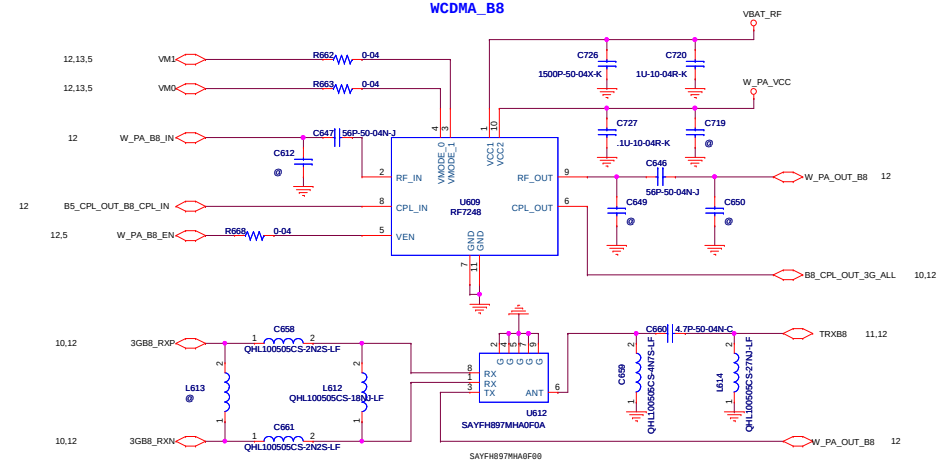
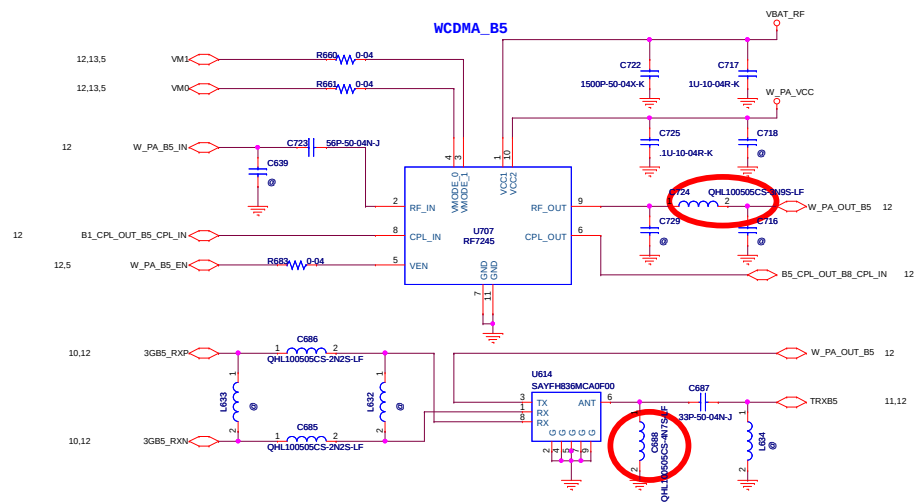
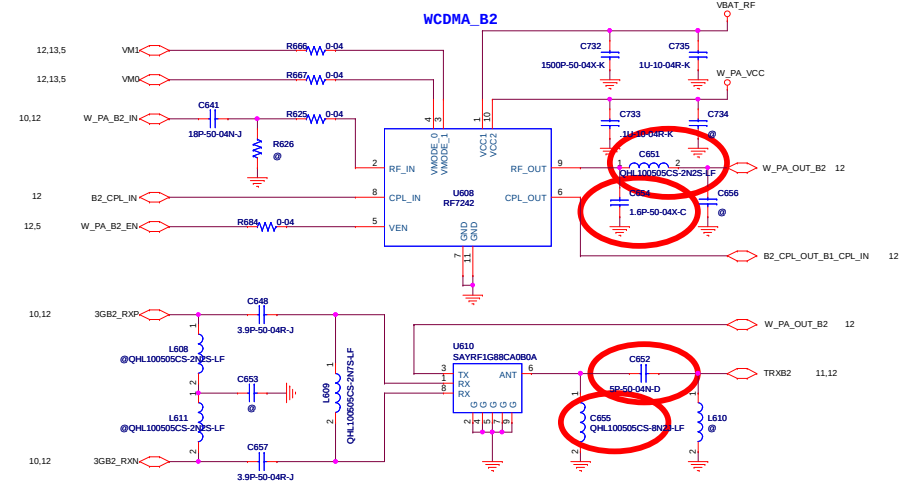
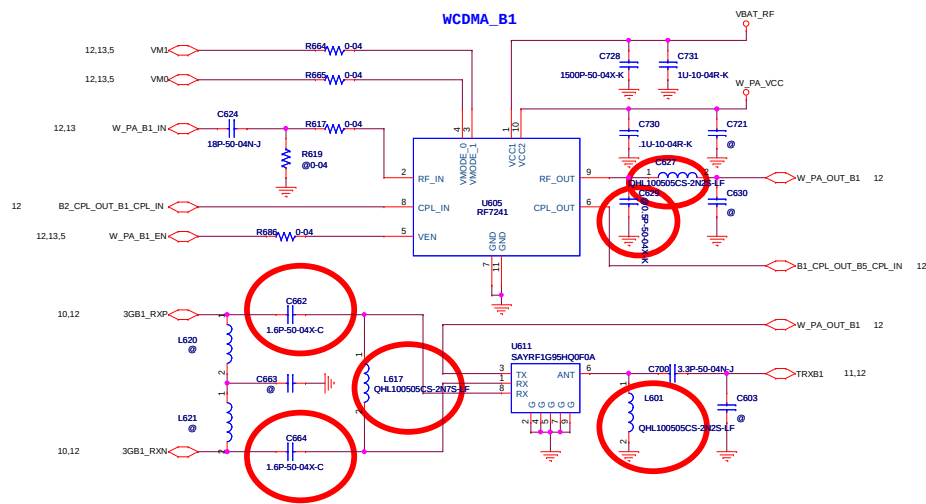


MODE \ Logic	DCX0_32K_EN	XMODE	VXODIG
VCTCX0	0(0ND)	0(0ND)	1(VI018)
DCX0 + 32K X0	0(0ND)	1(VI018)	1(VI018)
DCX0 + 32K-Less	1(VTXC028)	1(VTXC028)	1(VTXC028)

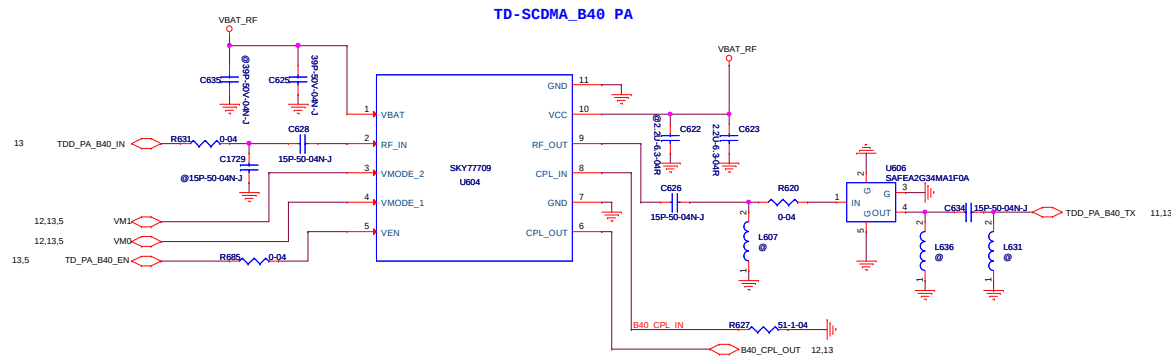
Net name	Pin name	Power domain	Max Current consumption (mA)
RXHF	F2	VRF18	100
VTXCO28	K1	VTXCO	6
VRXL	L7	VRF18	35
VTXLF	J11	VRF18	35
V28	E10	VTXCO	1
VTXHF	B11	VRF18	125
VIO18	K6	VIO18	1



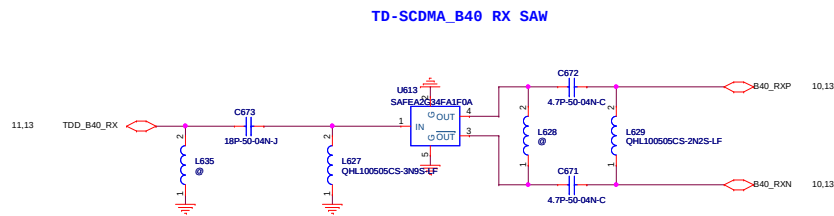
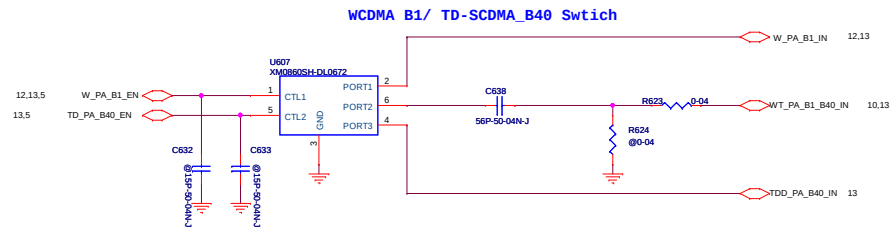
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Size	Document Number	Rev A	
Custom	TBxAmbox		
Date:	Sunday, October 27, 2013	Sheet	11 of 23

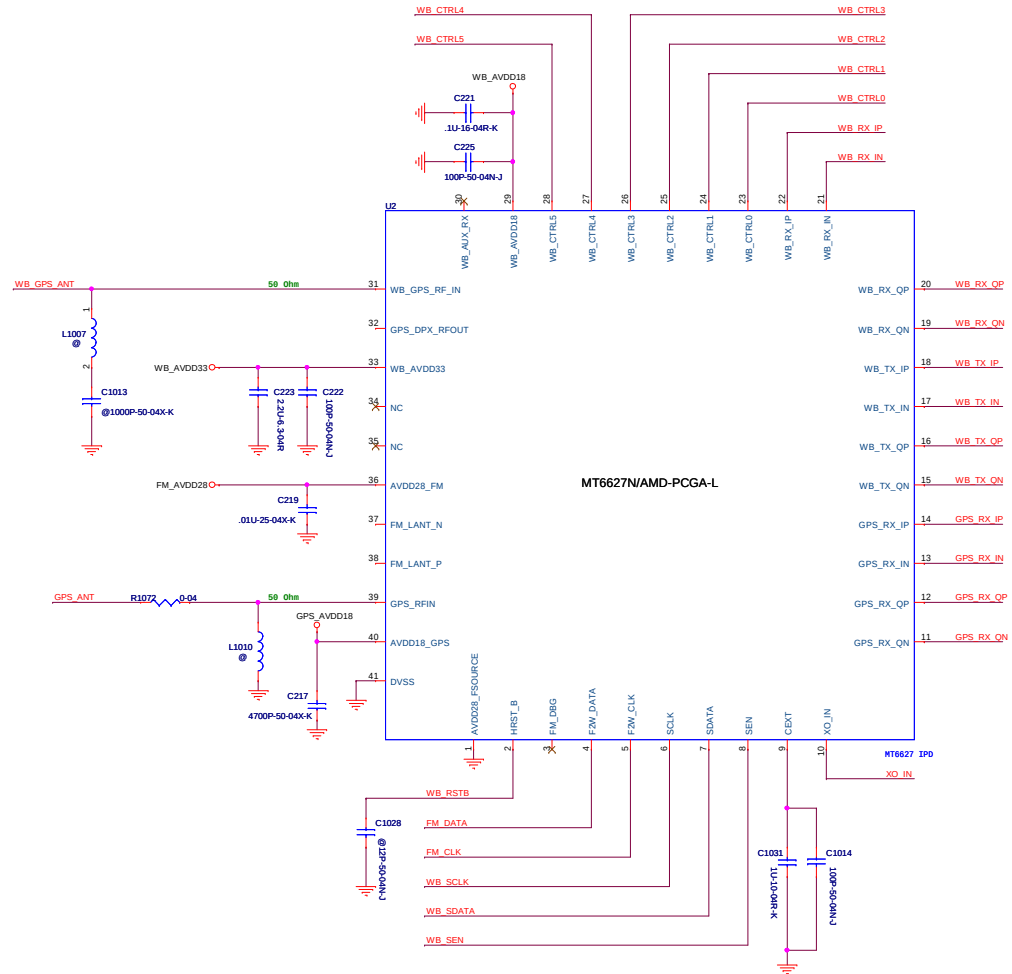
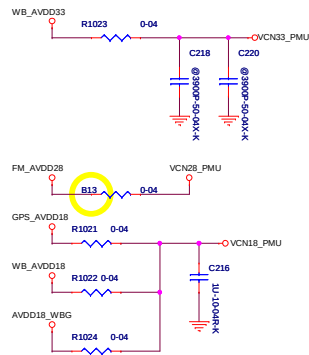
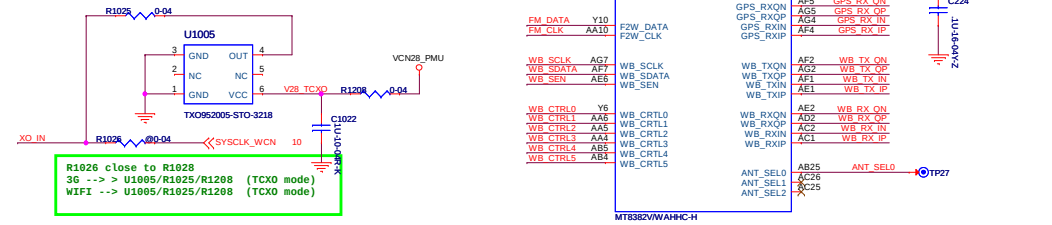


Title			
WCDMA PA			
Size	Document Number		Rev
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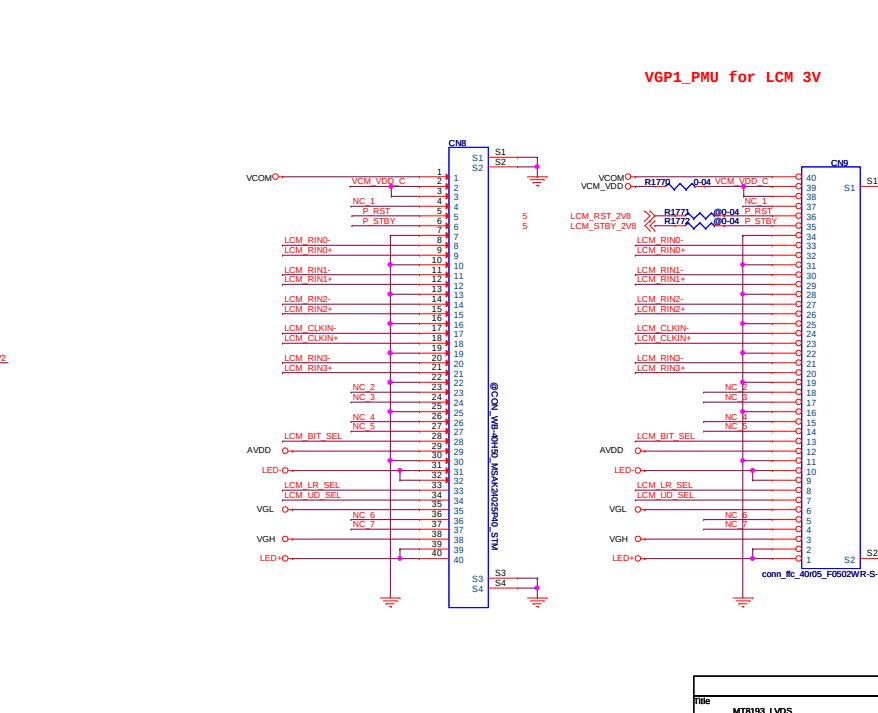
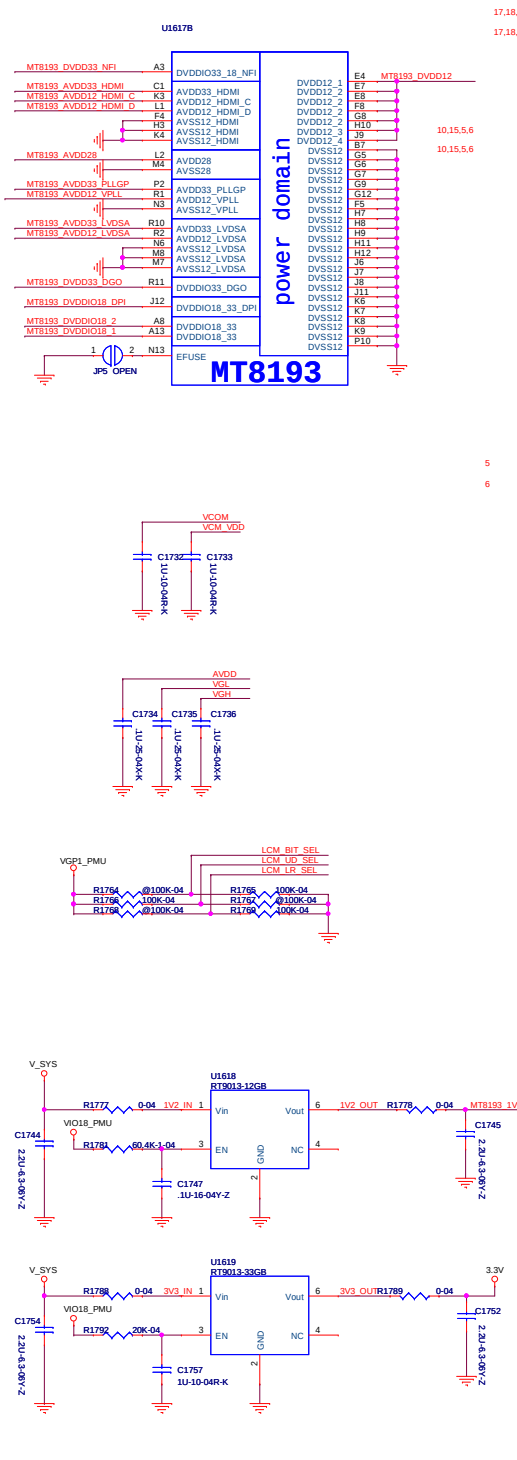
12.5	BPL_7	W_PA_B1_EN	12.13.5
5	BPL_11	TD_PA_B40_EN	13.5
12.5	VM1	VM1	12.13.5
12.5	VM0	VM0	12.13.5
10	WT_PA_B1_B40_IN	WT_PA_B1_B40_IN	10.13
12	W_PA_B1_IN	W_PA_B1_IN	12.13
11	TDD_PA_B40_TX	TDD_PA_B40_TX	11.13
12	B40_CPL_OUT	B40_CPL_OUT	12.13
10	B40_RXP	B40_RXP	10.13
10	B40_RXN	B40_RXN	10.13
11	TDD_B40_RX	TDD_B40_RX	11.13



[illegible]

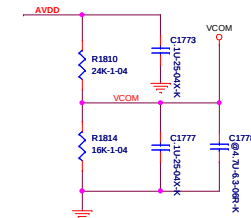
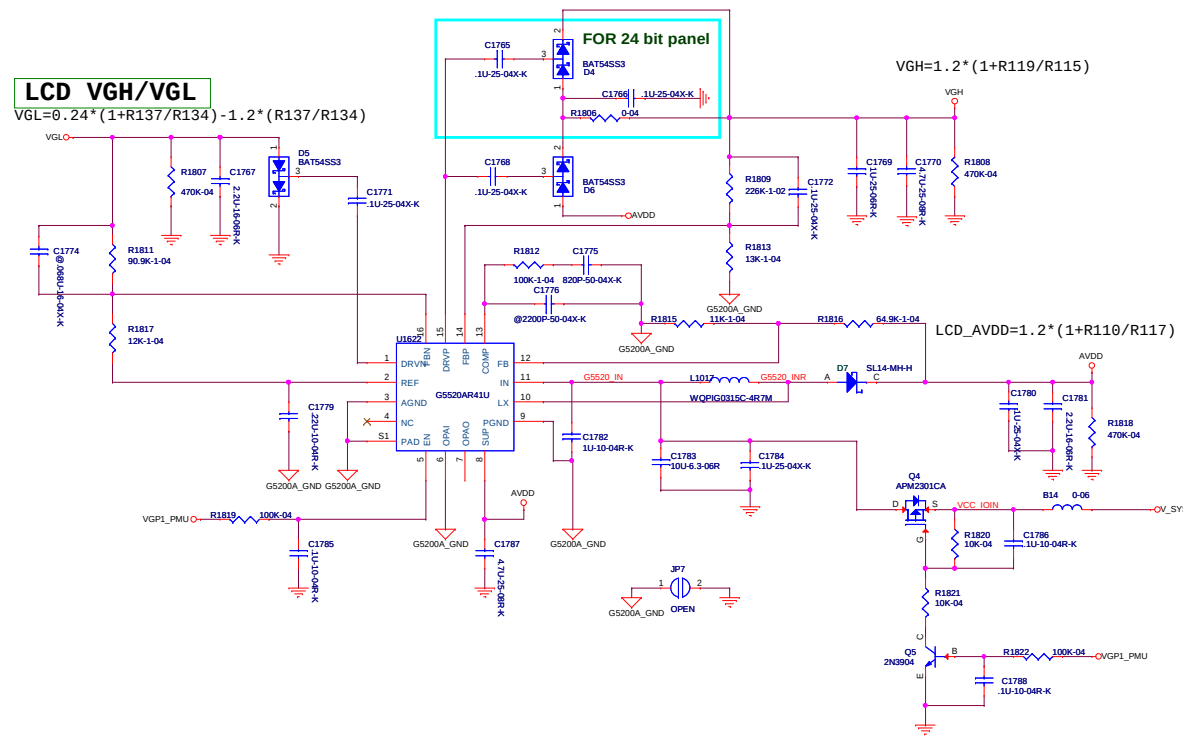
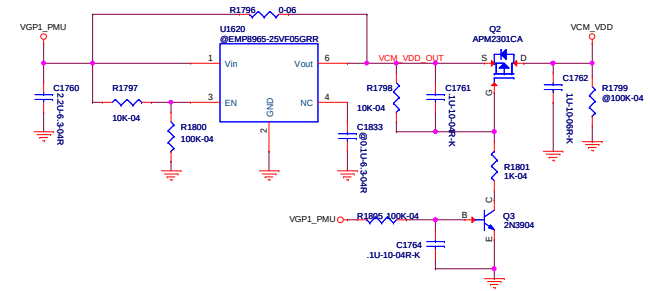
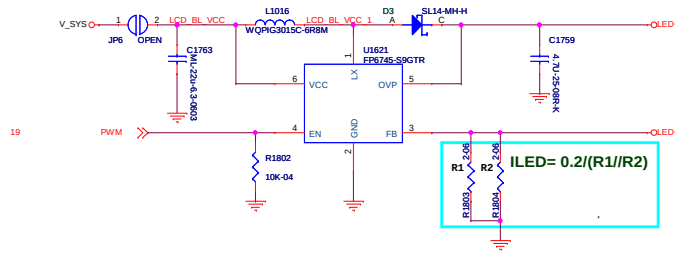
Title			
MT6627 WIF/VT/GPS			
Size	Document Number		Rev
Custom	TBxAmbx		A
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```
1:HDMI Mode;  
2:LCD Mode;
```



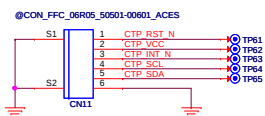
Title		
MT8193_LVDS		
Size	Document Number	Rev
Custom	TBxA00x	A
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7"	R108	1.3-06	153mA		
8"	R108	1.1-06	181mA		
9"	R108	1.5-06	R109	2-06	235mA
10"	R108	2-06	R109	2-06	200mA

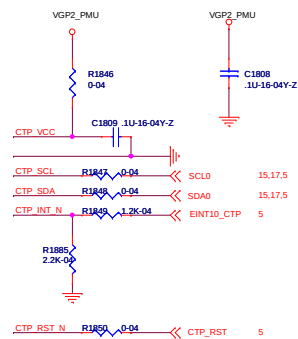


Chip on FPC

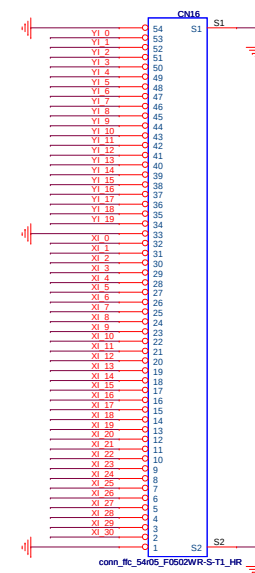
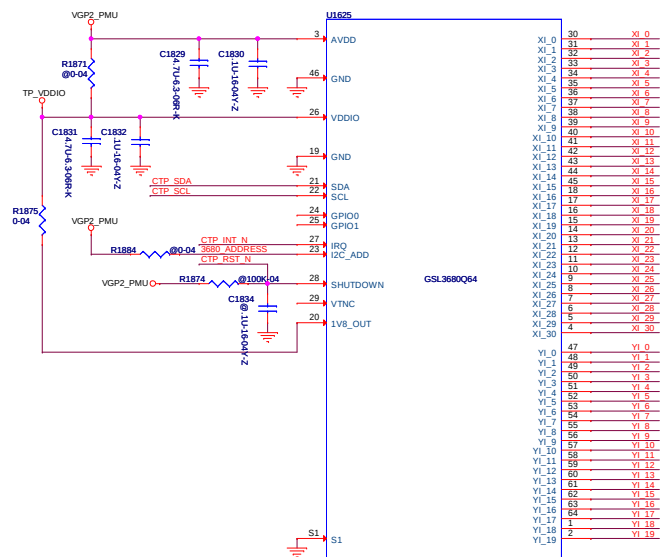
VGP2_PMU for CTP 2.8V



Touch Panel Connector

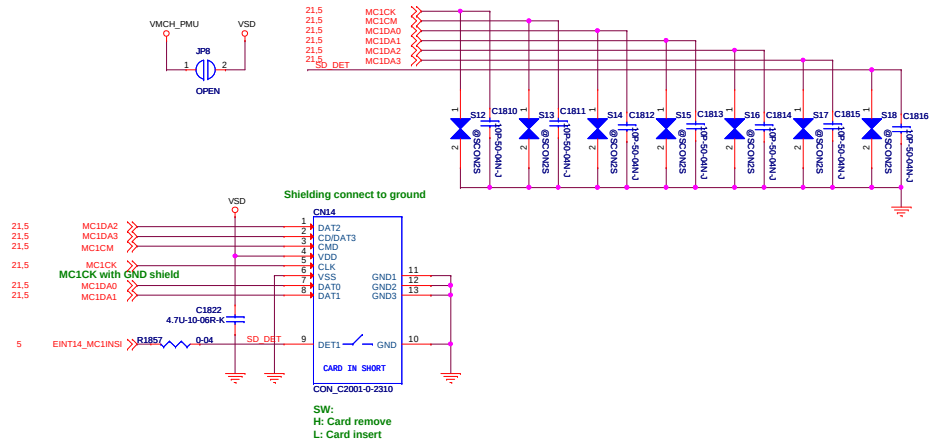


Chip on Board



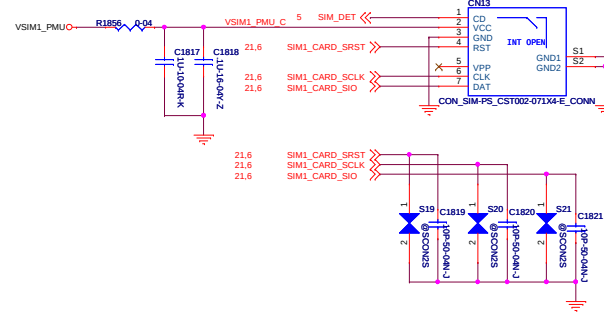
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SILEAD 3680 TP			
Size Custom	Document Number TBxAMox	Rev A	
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SD CARD



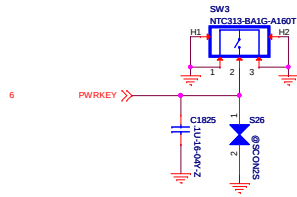
SIM

Connect to MD_EINT2 pin for SIM card hot-plug

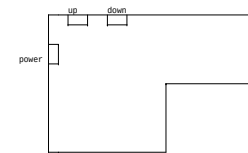
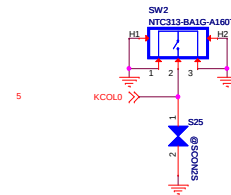


Title SD CARD/SIM CARD			
Size Custom	Document Number TBxAmbx		Rev A
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DO NOT put pull-up resistor on PWRKEY



Volume Up

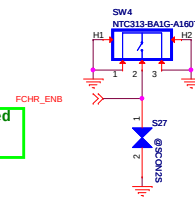


Notice :

1. Due to KC0L0 & KROW0 reset mode = GPIO input mode, "Force USB download mode" will be fail in KC0L0+KROW0.
So we change VolumeUp key=KC0L0+GND
2. Keypad matrix will become as
- | | | |
|-------|-------|-------|
| | KC0L0 | KC0L1 |
| KROW0 | | KEY_2 |

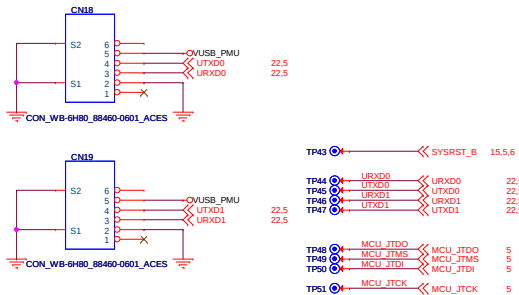
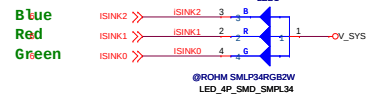
	KCOL0	KCOL1	KCOL2
KROW0	KEY 1	KEY 2	KEY 5
KROW1		KEY 3	KEY 6
KROW2		KEY 4	KEY 7

Volume Down



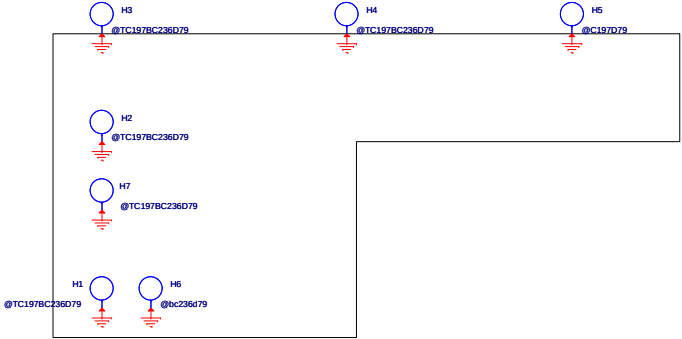
FCHR_ENB can be configured as a normal key function

Indicator LED



Net	Aux Func.0	Aux Func.1	Aux Func.2	Aux Func.3	Aux Func.4	Aux Func.5
JTMS	GPIO76	B1:JTMS	I1:CONN_MCU_TMS			
JTCK	GPIO77	I0:JTCK	I0:CONN_MCU_TCK1			
JTDI	GPIO78	I1:JTDI	I0:CONN_MCU_TDI			
JTDO	GPIO79	O:JTDO	O:CONN_MCU_TDO			
EINT1	GPIO1	O:PWM2	O:DPL_D5	I0:MD_EINT1	O:TDD_TDO	O:CONN_MCU_TDO
EINT2	GPIO2	O:CLKM0	O:DPL_D6	I0:MD_EINT2		O:CONN_MCU_DBGACK_N
EINT3	GPIO3	O:CLKM1	O:DPL_D7	I0:SPL_MI	I0:MD_EINT3	I1:CONN_MCU_DBGI_N
EINT4	GPIO4	O:CLKM2	O:DPL_D8	O:SPL_MO	I1:TDD_TCK	I0:CONN_MCU_TCK0
EINT5	GPIO5	I1:UCTS2	O:DPL_D9	O:SPL_CS	I1:TDD_TDI	I0:CONN_MCU_TDI
EINT6	GPIO6	O:URTS2	O:DPL_D10	O:SPL_CK	I0:TDD_TRSTN	I0:CONN_MCU_TRST_B
EINT7	GPIO7	I1:UCTS3	O:DPL_D11	B1:SDA1	I1:TDD_TMS	I1:CONN_MCU_TMS
ANT_SELO	GPIO47	O:ANT_SELO	O:PWM0	O:CONN_MCU_DBGACK_N		
ANT_SEL1	GPIO48	O:ANT_SEL1	O:PWM1	I1:CONN_MCU_DBGI_N		
ANT_SEL2	GPIO49	O:ANT_SEL2	O:PWM2	I0:CONN_MCU_TRST_B		

SCREW



Title			
SCREW / EPAD			
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Buck	Output Voltage(V)	Output Current(mA)	Input Decoupling	Output Decoupling	Notes
VPROC	0.7~1.4	2800	>10uF	L=0.68uH, C=10uF*4	Total output cap >40uF
VPA	0.5~3.4	600	>4.7uF	L=2.2uH, C=2.2uF+2.2uF	Output cap range 4.4uF +/-20%
VSYS	2.2	1200	>10uF	L=0.68uH, C=10uF*2	Total output cap >20uF

LDO	Output Voltage(V)	Output Current(mA)	Bypass cap	cap range	Notes
VA	2.8	150	1uF	-20%~+20%	Far-end bypass cap
VCN28	2.8	30	1uF	-20%~+20%	Far-end bypass cap
VTCXO	2.8	40	1uF	-20%~+20%	Far-end bypass cap
VCAMA	2.8	150	3.2uF	-20%~+20%	1uF near-end 2.2uF Far-end bypass cap
VCN33	3.3/3.4/3.5/3.6	240	4.7uF	-20%~+20%	Far-end bypass cap
VRTC	2.8	2	0.1uF to 1000uF	-20%~+20%	Far-end bypass cap
VM	1.24/1.39/1.54/1.84	700	10uF	-20%~+20%	Far-end bypass cap
VRF18	1.825	200	1uF	-20%~+200%	Far-end bypass cap
VIO18	1.8	300	4.7uF	-20%~+200%	Far-end bypass cap
VIO28	2.8	200	2.2uF	-20%~+200%	Far-end bypass cap
VCN18	1.8	120	1uF	-20%~+20%	Far-end bypass cap
VCAMD	1.2/1.3/1.5/1.8	150	1uF	-20%~+20%	Far-end bypass cap
VCAM_IO	1.8	100	1uF	-20%~+20%	Far-end bypass cap
VEMC_3V3	3.0/3.3	400	4.7uF	-20%~+20%	Far-end bypass cap
VMC	1.8/3.3	100	1uF	-20%~+20%	Far-end bypass cap
VMCH	3.0/3.3	400	2.2uF	-20%~+20%	Far-end bypass cap
VUSB	3.3	20	1uF	-20%~+20%	Far-end bypass cap
VSIM1	1.8/3.0	50	1uF	-20%~+20%	Far-end bypass cap
VSIM2	1.8/3.0	50	1uF	-20%~+20%	Far-end bypass cap
VGP1	1.2/1.3/1.5/1.8/2.0/2.8/3.0/3.3	100	1uF	-20%~+20%	Far-end bypass cap
VIBR	1.2/1.3/1.5/1.8/2.0/2.8/3.0/3.3	100	1uF	-20%~+20%	Far-end bypass cap
VGP2	1.2/1.3/1.5/1.8/2.0/2.5/2.8/3.0	100	1uF	-20%~+20%	Far-end bypass cap
VGP3	1.2/1.3/1.5/1.8	200	1uF	-20%~+20%	Far-end bypass cap
VCAM_AF	1.2/1.3/1.5/1.8/2.0/2.8/3.0/3.3	100	1uF	-20%~+20%	Far-end bypass cap
VDIG18	1.8	20	1uF	-20%~+20%	Far-end bypass cap