

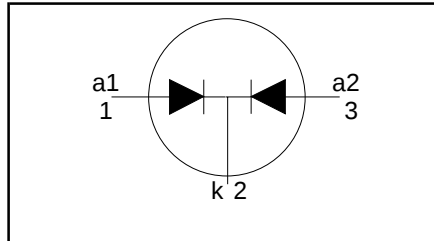
Rectifier diodes Schottky barrier

PBYR245CT series

FEATURES

- Low forward volt drop
- Fast switching
- Reverse surge capability
- High thermal cycling performance
- low profile surface mounting package

SYMBOL



QUICK REFERENCE DATA

$$V_R = 40 \text{ V} / 45 \text{ V}$$

$$I_{O(AV)} = 2 \text{ A}$$

$$V_F \leq 0.45 \text{ V}$$

GENERAL DESCRIPTION

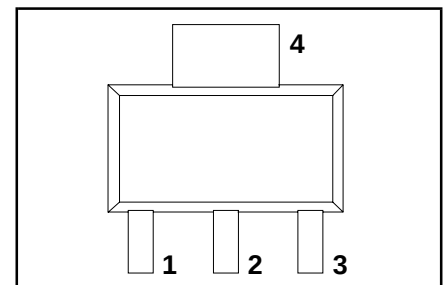
Dual, common cathode schottky rectifier diodes in a plastic envelope. Intended for use as output rectifiers in low voltage, high frequency switched mode power supplies.

The PBYR245CT series is supplied in the surface mounting SOT223 package.

PINNING

PIN	DESCRIPTION
1	anode 1
2	cathode
3	anode 2
tab	cathode

SOT223



LIMITING VALUES

Limiting values in accordance with the Absolute Maximum System (IEC 134)

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.		UNIT
V_{RRM}	Peak repetitive reverse voltage	PBYR2 $T_{sp} \leq 74^\circ\text{C}$ square wave; $\delta = 0.5$; $T_{sp} \leq 119^\circ\text{C}$	-	40CT 40	45CT 45	V
V_{RWM}	Working peak reverse voltage		-	40	45	V
V_R	Continuous reverse voltage		-	40	45	V
$I_{O(AV)}$	Average rectified output current (both diodes conducting)		-	2		A
I_{FRM}	Repetitive peak forward current per diode	square wave; $\delta = 0.5$; $T_{sp} \leq 119^\circ\text{C}$ $t = 10 \text{ ms}$ $t = 8.3 \text{ ms}$ sinusoidal; $T_j = 125^\circ\text{C}$ prior to surge; with reapplied $V_{RRM(max)}$ pulse width and repetition rate limited by T_{jmax}	-	2		A
I_{FSM}	Non-repetitive peak forward current per diode		-	6		A
			-	6.6		A
I_{RRM}	Peak repetitive reverse surge current per diode		-	1		A
T_j	Operating junction temperature		-	150		$^\circ\text{C}$
T_{stg}	Storage temperature		- 40	150		$^\circ\text{C}$

THERMAL RESISTANCES

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
$R_{th j-a}$	Thermal resistance junction to ambient	pcb mounted, minimum footprint pcb mounted, pad area as in fig:1	-	156 70	-	K/W K/W

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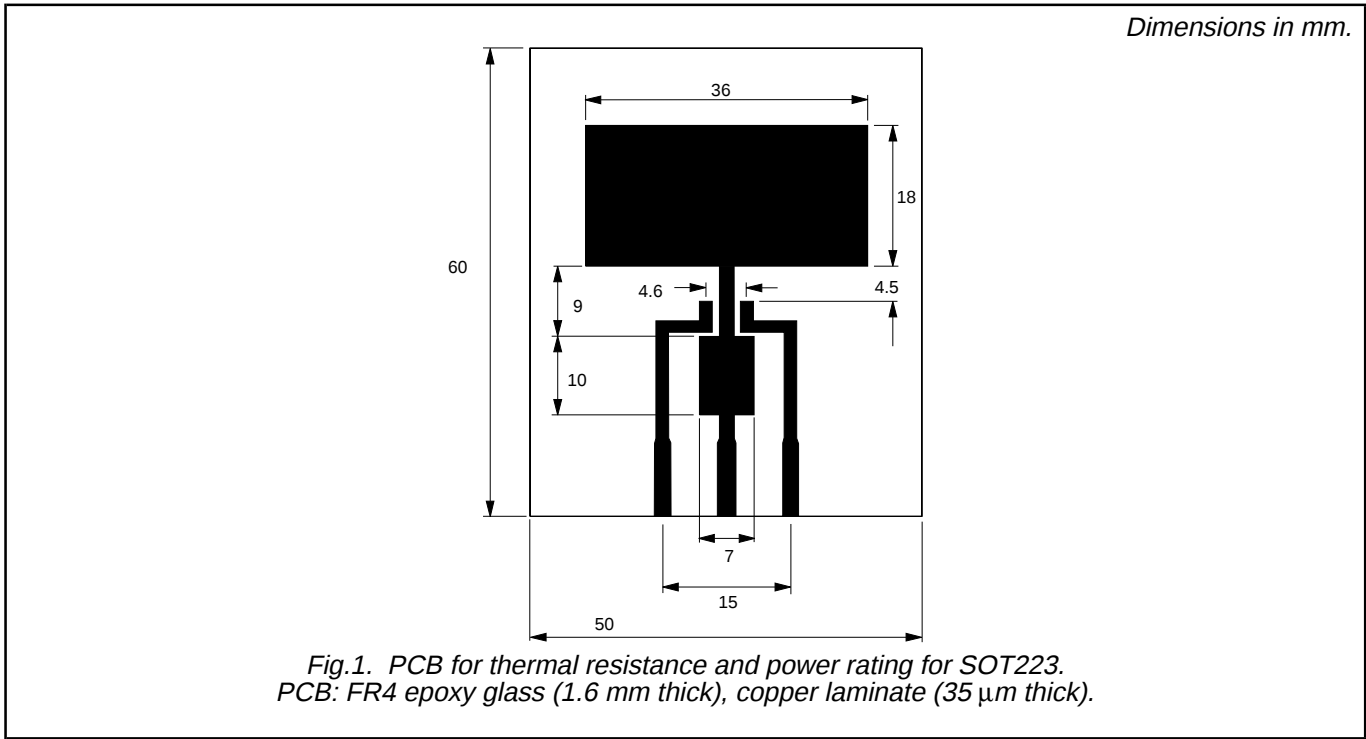
PBYR245CT series

ELECTRICAL CHARACTERISTICS

T_j = 25 °C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
V _F	Forward voltage	I _F = 1 A; T _j = 125°C	-	0.41	0.45	V
		I _F = 2 A	-	0.58	0.7	V
I _R	Reverse current	V _R = V _{RWM}	-	0.03	0.2	mA
		V _R = V _{RWM} ; T _j = 100°C	-	1.5	10	mA
C _d	Junction capacitance	V _R = 5 V; f = 1 MHz, T _j = 25°C to 125°C	-	60	-	pF

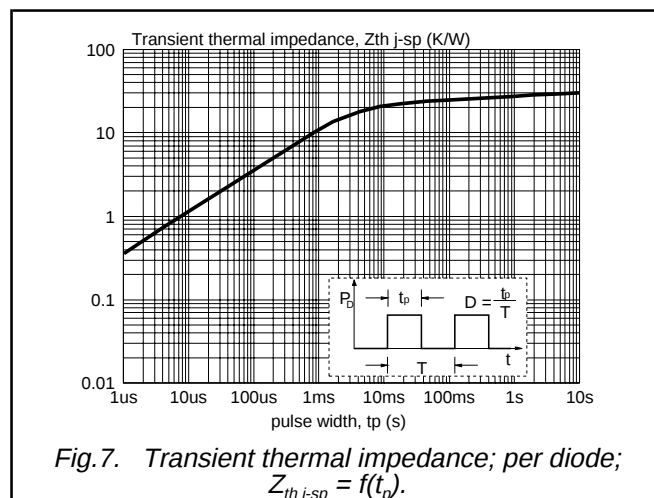
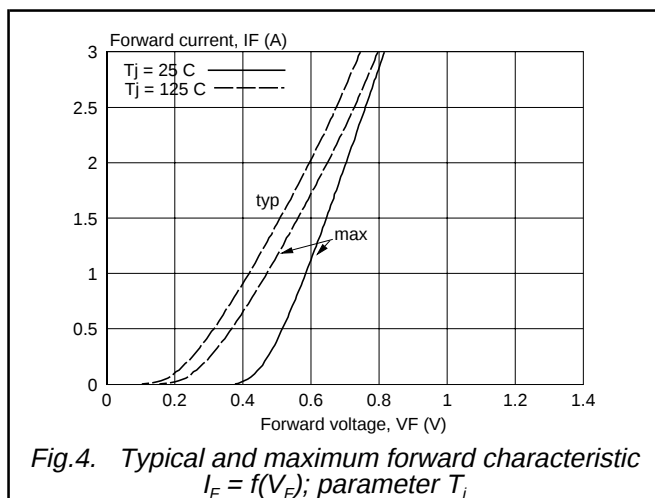
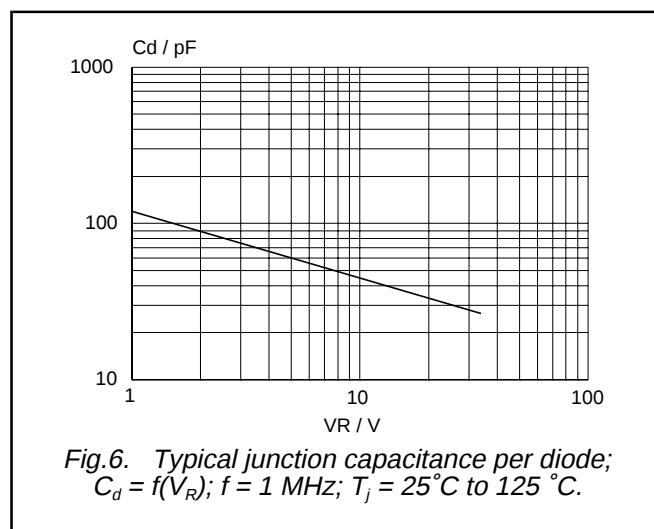
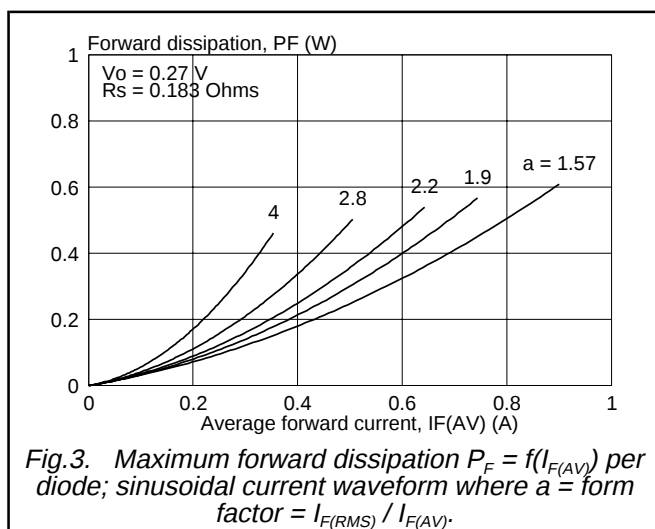
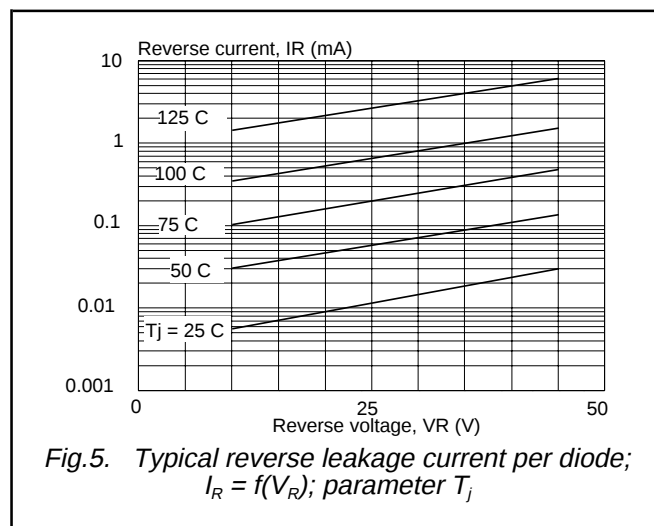
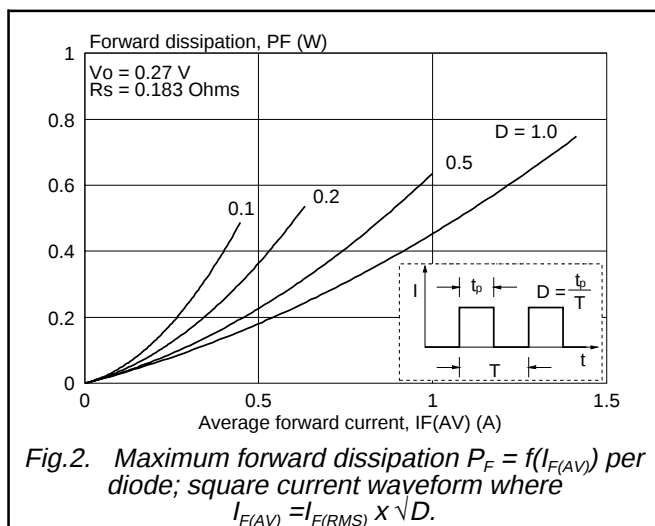
PRINTED CIRCUIT BOARD



Rectifier diodes

Schottky barrier

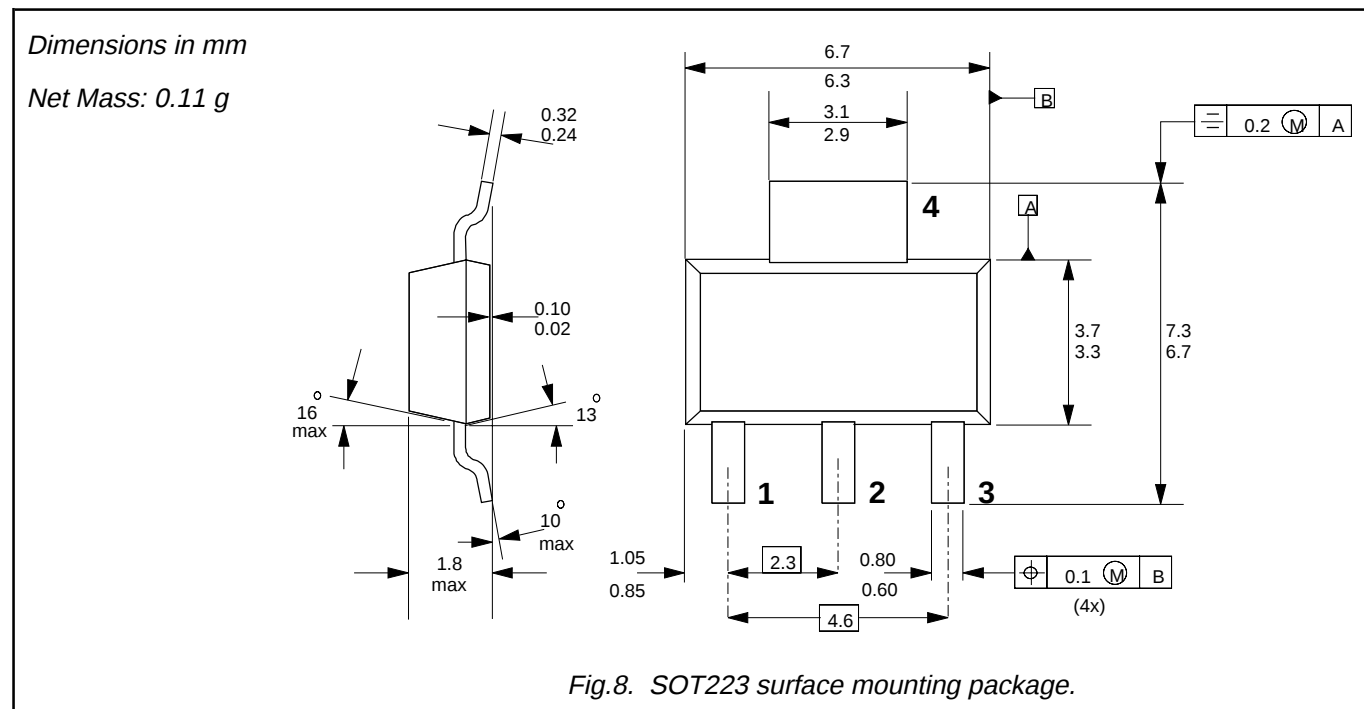
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MECHANICAL DATA



Notes

1. For further information, refer to Philips publication SC18 "SMD Footprint Design and Soldering Guidelines".
Order code: 9397 750 00505.
2. Epoxy meets UL94 V0 at 1/8".

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DEFINITIONS

Data sheet status	
Objective specification	This data sheet contains target or goal specifications for product development.
Preliminary specification	This data sheet contains preliminary data; supplementary data may be published later.
Product specification	This data sheet contains final product specifications.
Limiting values	
Limiting values are given in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of this specification is not implied. Exposure to limiting values for extended periods may affect device reliability.	
Application information	
Where application information is given, it is advisory and does not form part of the specification.	
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