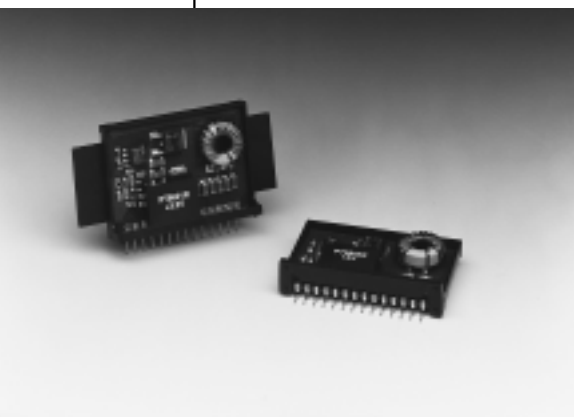


PT6600 Series**9 AMP ADJUSTABLE
INTEGRATED SWITCHING REGULATOR**

[Application Notes](#)
[Mechanical Outline](#)
[Product Selector Guide](#)

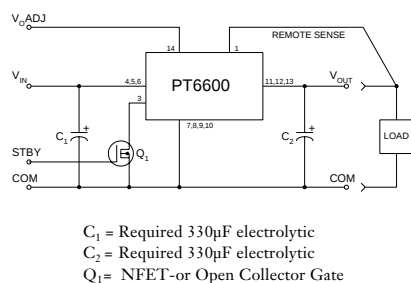


- Single Device 9A Output
- Input Voltage Range: 3.1V to 6.0V
- Adjustable Output Voltage
- 90% Efficiency
- Remote Sense Capability
- Standby Function
- Over-Temperature Protection

The PT6600 series is a new addition to the Power Trends' high

performance family of 14-Pin SIP (Single In-line Package) Integrated Switching Regulators (ISRs), designed for stand alone operation in applications requiring as much as 9A of output current (10A with a side heat tab).

Only two external capacitors are required for proper operation. Please note that this product does not include short circuit protection.

Standard Application**Pin-Out Information**

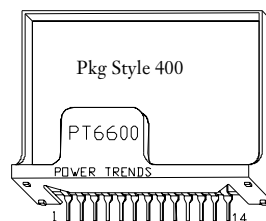
Pin	Function
1	Remote Sense
2	Do not connect
3	STBY*-Standby
4	V_{in}
5	V_{in}
6	V_{in}
7	GND
8	GND
9	GND
10	GND
11	V_{out}
12	V_{out}
13	V_{out}
14	V_{out} Adjust

Ordering Information

PT6601□ = +3.3 Volts
 †PT6602□ = +1.5 Volts
 PT6603□ = +2.5 Volts
 PT6604□ = +3.6 Volts
 †PT6605□ = +1.2 Volts
 †PT6606□ = +1.8 Volts
 †3.3V Input Bus Capable

PT Series Suffix (PT1234X)

Case/Pin Configuration	Heat Spreader	Heat Spreader with Side Tabs
Vertical Through-Hole	P	R
Horizontal Through-Hole	D	G
Horizontal Surface Mount	E	B



Note: Back surface of product is conducting metal.

Specifications

Characteristics ($T_a = 25^\circ\text{C}$ unless noted)	Symbols	Conditions	PT6600 SERIES			
			Min	Typ	Max	Units
Output Current	I_o	$T_a = 60^\circ\text{C}$, 200 LFM, pkg P $T_a = 25^\circ\text{C}$, natural convection	0.1* 0.1*	— —	9.0** 7.0**	A A
Input Voltage Range	V_{in}	$0.1\text{A} \leq I_o \leq 8.0\text{A}$ $V_o = +2.5/3.3\text{V}$ $V_o = +1.5\text{V}$ $V_o = +3.6\text{V}$	4.5 3.1 4.8	— — —	6.0 6.0 6.0	V
Output Voltage Tolerance	ΔV_o	$V_{in} = +5\text{V}$, $I_o = 8.0\text{A}$ $T_a = 0^\circ\text{C}$ to 65°C	$V_o - 0.1$	—	$V_o + 0.1$	V
Output Voltage Adjust Range	V_{oadj}	Pin 14 to V_o or ground $V_{in\min} = +3.1\text{V}$ or $V_o + 1.2\text{V}$ (whichever is greater) $V_o = +3.3\text{V}$ $V_o = +1.5\text{V}$ $V_o = +2.5\text{V}$ $V_o = +3.6\text{V}$	2.25 1.27 1.80 2.50	— — — —	4.20 2.65 3.50 4.30	V
Line Regulation	Reg_{line}	$4.5\text{V} \leq V_{in} \leq 6.0\text{V}$, $I_o = 8.0\text{A}$ $3.1\text{V} \leq V_{in} \leq 6.0\text{V}$, $I_o = 8.0\text{A}$ $4.5\text{V} \leq V_{in} \leq 6.0\text{V}$, $I_o = 8.0\text{A}$ $V_o = +3.3\text{V}$ $V_o = +1.5\text{V}$ $V_o = +2.5\text{V}$	— — —	± 7 ± 3 ± 7	± 17 ± 8 ± 13	mV
Load Regulation	Reg_{load}	$V_{in} = +5\text{V}$, $0.1 \leq I_o \leq 8.0\text{A}$ $V_o = +3.3\text{V}$ $V_o = +1.5\text{V}$ $V_o = +2.5\text{V}$	— — —	± 17 ± 12 ± 13	± 33 ± 23 ± 25	mV
V_o Ripple/Noise	V_n	$V_{in} = 5\text{V}$, $I_o = 8.0\text{A}$	—	50	—	mVpp
Transient Response with $C_2 = 330\mu\text{F}$	t_{tr} V_{os}	I_o step between 4.0A and 8.0A V_o over/undershoot	— —	100 150	— —	μSec mV
Efficiency	η	$V_{in} = +5\text{V}$, $I_o = 3.0\text{A}$ $V_o = +3.3/3.6\text{V}$	—	90	—	%
		$V_o = +1.5\text{V}$	—	76	—	%
		$V_o = +2.5\text{V}$	—	85	—	%
		$V_{in} = +5\text{V}$, $I_o = 8.0\text{A}$ $V_o = +3.3/3.6\text{V}$	—	83	—	%
		$V_o = +1.5\text{V}$	—	68	—	%
		$V_o = +2.5\text{V}$	—	76	—	%

* ISR will operate down to no load with reduced specifications.

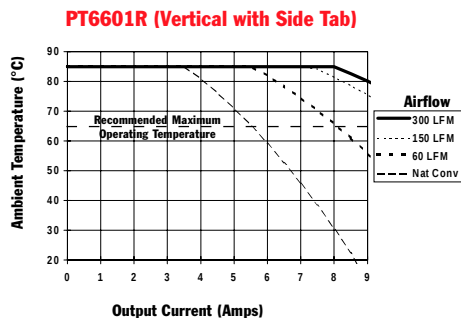
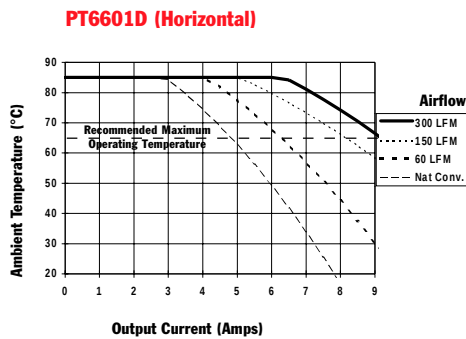
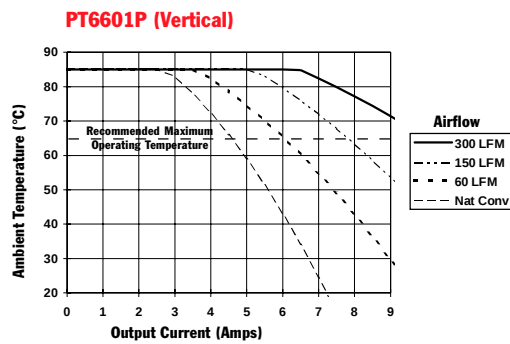
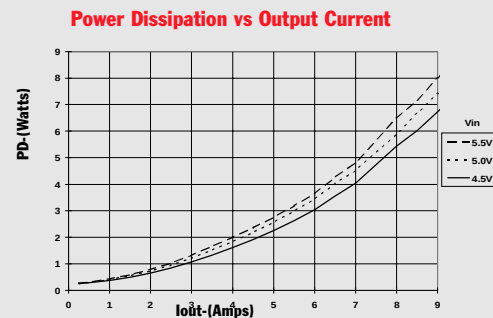
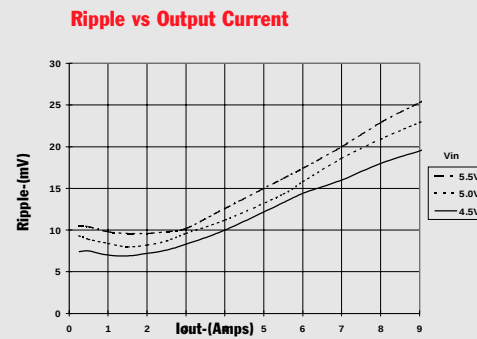
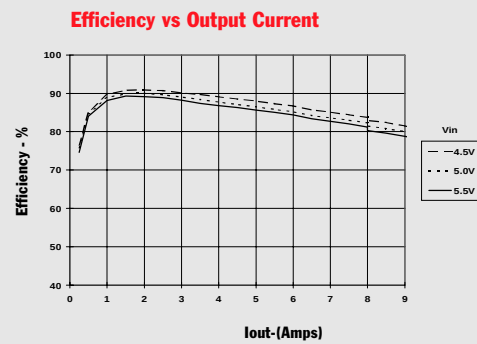
** See SOA curves

Note: The PT6600 Series requires two 330µF electrolytic capacitors (input and output) for proper operation in all applications.
 See PT6000/7000 Series Capacitor application note.

PT6600 Series**Specifications (continued)**

Characteristics ($T_a = 25^\circ\text{C}$ unless noted)	Symbols	Conditions	PT6600 SERIES			
			Min	Typ	Max	Units
Switching Frequency	f_o	$3.1\text{V} \leq V_{in} \leq 6.0\text{V}$ $0.1\text{A} \leq I_o \leq 8.0\text{A}$	475	600	725	kHz
Absolute Maximum Operating Temperature Range	T_a		0	—	+85	$^\circ\text{C}$
Recommended Operating Temperature Range	T_a	Free Air Convection (40-60 LFM) Over V_{in} and I_o ranges with heat tab	0	—	65**	$^\circ\text{C}$
Thermal Resistance	θ_{ja}	Free Air Convection (40-60 LFM)	—	25	—	$^\circ\text{C/W}$
Storage Temperature	T_s	—	-40	—	+125	$^\circ\text{C}$
Mechanical Shock	—	Per Mil-STD-883D, Method 2002.3	—	500	—	G's
Mechanical Vibration	—	Per Mil-STD-883D, Method 2007.2, 20-2000 Hz, soldered in a PC board	—	7.5	—	G's
Weight	—	—	—	14	—	grams

** See SOA curves

Note: The PT6600 Series requires two 330 μF electrolytic capacitors (input and output) for proper operation in all applications.**CHARACTERISTIC DATA****Safe Operating Area Curves (@ $V_{in}=+5.0\text{V}$)** (See Note 2)**PT6601, 3.3 VDC** (See Note 1)**Note 1:** All data listed in the above graphs has been developed from actual products tested at 25°C . This data is considered typical data for the ISR.**Note 2:** SOA curves represent operating conditions at which internal components are at or below manufacturer's maximum rated operating temperatures.

Adjusting the Output Voltage of the PT6500 and PT6600 5V Bus Converters

The output voltage of the Power Trends PT6500/PT6600 Series ISRs may be adjusted higher or lower than the factory trimmed pre-set voltage with the addition of a single external resistor. Table 1 accordingly gives the allowable adjustment range for each model in the series as V_a (min) and V_a (max).

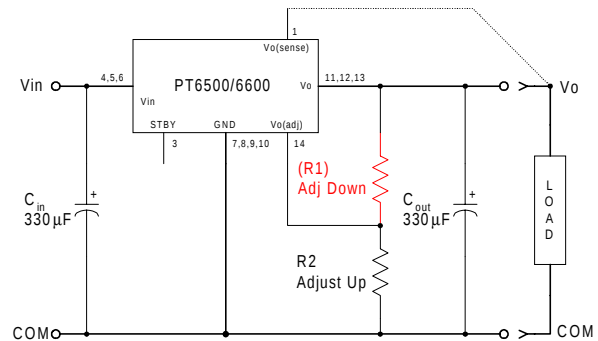
Adjust Up: An increase in the output voltage is obtained by adding a resistor R2, between pin 14 (V_o adjust) and pins 7-10 (GND).

Adjust Down: Add a resistor (R1), between pin 14 (V_o adjust) and pins 11-13 (V_{out}).

Refer to Figure 1 and Table 2 for both the placement and value of the required resistor, either (R1) or R2 as appropriate.

Notes:

1. Use only a single 1% resistor in either the (R1) or R2 location. Place the resistor as close to the ISR as possible.
2. Never connect capacitors from V_o adjust to either GND, V_{out} , or the Remote Sense pin. Any capacitance added to the V_o adjust pin will affect the stability of the ISR.
3. If the Remote Sense feature is being used, connecting the resistor (R1) between pin 14 (V_o adjust) and pin 1 (Remote Sense) can benefit load regulation.
4. The minimum input voltage required by the part is $V_{out} + 1.2$ or $3.1V$, whichever is higher.

Figure 1

The values of (R1) [adjust down], and R2 [adjust up], can also be calculated using the following formulae.

$$(R1) = \frac{R_o (V_a - 1.0)}{(V_o - V_a)} - R_s \text{ k}\Omega$$

$$R2 = \frac{R_o}{V_a - V_o} - R_s \text{ k}\Omega$$

Where: V_o = Original output voltage
 V_a = Adjusted output voltage
 R_o = The resistance value in Table 1
 R_s = The series resistance from Table 1

Table 1**PT6500/6600 ADJUSTMENT AND FORMULA PARAMETERS**

Series Pt #	PT6505	PT6507	PT6502	PT6508	PT6506	PT6503	PT6501	PT6504
	PT6605	PT6607	PT6602	PT6608	PT6606	PT6603	PT6601	PT6604
V_o (nom)	1.2	1.3	1.5	1.7	1.8	2.5	3.3	3.6
V_a (min)	1.14	1.19	1.27	1.36	1.4	1.8	2.25	2.5
V_a (max)	2.35	2.45	2.65	2.85	2.95	3.5	4.2	4.3
R_o (k Ω)	2.49	2.49	2.49	2.49	2.49	4.99	12.1	10.0
R_s (k Ω)	2.0	2.0	2.0	2.0	2.0	4.22	12.1	12.1

PT6500/PT6600 Series**Application****Notes****Table 2****PT6500/PT6600 ADJUSTMENT RESISTOR VALUES**

Series Pt #	PT6505	PT6507	PT6502	PT6508	PT6506	PT6503	PT6501	PT6504
	PT6605	PT6607	PT6602	PT6608	PT6606	PT6603	PT6601	PT6604
V_o (nom)	1.2	1.3	1.5	1.7	1.8	2.5	3.3	3.6
V_a (req'd)								
1.15	(5.5)k Ω							
1.2		(3.0)k Ω						
1.25	47.8k Ω	(10.5)k Ω						
1.3	22.9k Ω		(1.7)k Ω					
1.35	14.6k Ω	47.8k Ω	(3.8)k Ω					
1.4	10.5k Ω	22.9k Ω	(8.0)k Ω	(1.3)k Ω	(0.5)k Ω			
1.45	8.0k Ω	14.6k Ω	(20.4)k Ω	(2.5)k Ω	(1.2)k Ω			
1.5	6.3k Ω	10.5k Ω		(4.2)k Ω	(2.2)k Ω			
1.55	5.1k Ω	8.0k Ω	47.8k Ω	(7.1)k Ω	(3.5)k Ω			
1.6	4.2k Ω	6.3k Ω	22.9k Ω	(12.9)k Ω	(5.5)k Ω			
1.65	3.5k Ω	4.1k Ω	14.6k Ω	(30.4)k Ω	(8.8)k Ω			
1.7	3.0k Ω	4.2k Ω	10.5k Ω		(15.4)k Ω			
1.75	2.5k Ω	3.5k Ω	8.0k Ω	47.8k Ω	(35.4)k Ω			
1.8	2.2k Ω	3.0k Ω	6.3k Ω	22.9k Ω		(1.5)k Ω		
1.85	1.8k Ω	2.5k Ω	5.1k Ω	14.6k Ω	47.8k Ω	(2.3)k Ω		
1.9	1.6k Ω	2.2k Ω	4.2k Ω	10.5k Ω	22.9k Ω	(3.3)k Ω		
1.95	1.3k Ω	1.8k Ω	3.5k Ω	8.0k Ω	14.6k Ω	(4.4)k Ω		
2.0	1.1k Ω	1.6k Ω	3.0k Ω	6.3k Ω	10.5k Ω	(5.8)k Ω		
2.05	0.9k Ω	1.3k Ω	2.5k Ω	5.1k Ω	8.0k Ω	(7.4)k Ω		
2.1	0.8k Ω	1.1k Ω	2.2k Ω	4.2k Ω	6.3k Ω	(9.5)k Ω		
2.15	0.6k Ω	0.9k Ω	1.8k Ω	3.5k Ω	5.1k Ω	(12.2)k Ω		
2.2	0.5k Ω	0.8k Ω	1.6k Ω	3.0k Ω	4.2k Ω	(15.7)k Ω		
2.25	0.4k Ω	0.6k Ω	1.3k Ω	2.5k Ω	3.5k Ω	(20.7)k Ω	(2.3)k Ω	
2.3	0.3k Ω	0.5k Ω	1.1k Ω	2.2k Ω	3.0k Ω	(28.2)k Ω	(3.6)k Ω	
2.35	0.2k Ω	0.4k Ω	0.9k Ω	1.8k Ω	2.5k Ω	(40.7)k Ω	(5.1)k Ω	
2.4		0.3k Ω	0.8k Ω	1.6k Ω	2.2k Ω	(65.6)k Ω	(6.7)k Ω	
2.45		0.2k Ω	0.6k Ω	1.3k Ω	1.8k Ω	(140.0)k Ω	(8.5)k Ω	
2.5			0.5k Ω	1.1k Ω	1.6k Ω		(10.6)k Ω	(1.5)k Ω
2.55			0.4k Ω	0.9k Ω	1.3k Ω	95.6k Ω	(12.9)k Ω	(2.7)k Ω
2.6			0.3k Ω	0.8k Ω	1.1k Ω	45.7k Ω	(15.6)k Ω	(3.9)k Ω
2.65			0.2k Ω	0.6k Ω	0.9k Ω	29.0k Ω	(18.6)k Ω	(5.3)k Ω
2.7				0.5k Ω	0.8k Ω	20.7k Ω	(22.2)k Ω	(6.8)k Ω
2.75				0.4k Ω	0.6k Ω	15.7k Ω	(26.4)k Ω	(8.5)k Ω
2.8				0.3k Ω	0.5k Ω	12.4k Ω	(31.5)k Ω	(10.4)k Ω
2.85				0.2k Ω	0.4k Ω	10.0k Ω	(37.6)k Ω	(12.6)k Ω
2.9					0.3k Ω	8.3k Ω	(45.4)k Ω	(15.0)k Ω
2.95					0.2k Ω	0.9k Ω	(55.3)k Ω	(17.9)k Ω
3.0						5.8k Ω	(68.6)k Ω	(21.2)k Ω
3.1						4.1k Ω	(115.0)k Ω	(29.9)k Ω
3.2						2.9k Ω	(254.0)k Ω	(42.9)k Ω
3.3						2.0k Ω		(64.6)k Ω
3.4						1.3k Ω	109.0k Ω	(108.0)k Ω
3.5						0.8k Ω	48.4k Ω	(238.0)k Ω
3.6							28.2k Ω	
3.7							18.2k Ω	87.9k Ω
3.8							12.1k Ω	37.9k Ω
3.9	4/. $V_{out} > 3.8V_{dc}$ requires $V_{in} > 5.0V_{dc}$!						8.1k Ω	21.2k Ω
4.0							5.2k Ω	12.9k Ω
4.1							3.0k Ω	7.9k Ω
4.2							1.3k Ω	4.6k Ω
4.3								2.2k Ω

R1 = (Red) R2 = Black

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