To our customers,

Old Company Name in Catalogs and Other Documents

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Renesas Electronics website: http://www.renesas.com

April 1st, 2010
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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RJP4003ASA

Nch IGBT for Strobe Flash

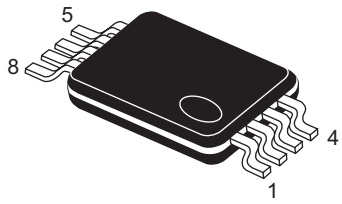
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Rev.2.00
Nov 10, 2008

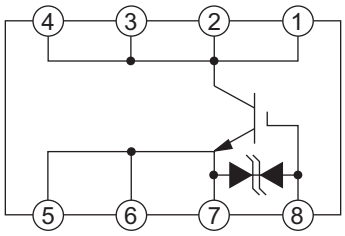
Features

- Small surface mount package (TSSOP-8)
- V_{CES} : 400 V
- I_{CM} : 150 A
- Drive voltage : 4 V

Outline

RENESAS Package code: PTSP0008JB-B
(Package name: TSSOP-8 <TTP-8DV>)





1, 2, 3, 4 : Collector
5, 6, 7 : Emitter
8 : Gate

Applications

Strobe flash for cameras

Maximum Ratings

(Tc = 25°C)

Parameter	Symbol	Ratings	Unit	Conditions
Collector-emitter voltage	V_{CES}	400	V	V_{GE} = 0 V
Gate-emitter voltage	V_{GES}	±6	V	V_{CE} = 0 V
Peak gate-emitter voltage	V_{GEM}	±8	V	V_{CE} = 0 V, tw = 10 s
Collector current (Pulse)	I_{CM}	150	A	C_M = 400 µF (see performance curve)
Junction temperature	Tj	– 40 to +150	°C	
Storage temperature	Tstg	– 40 to +150	°C	

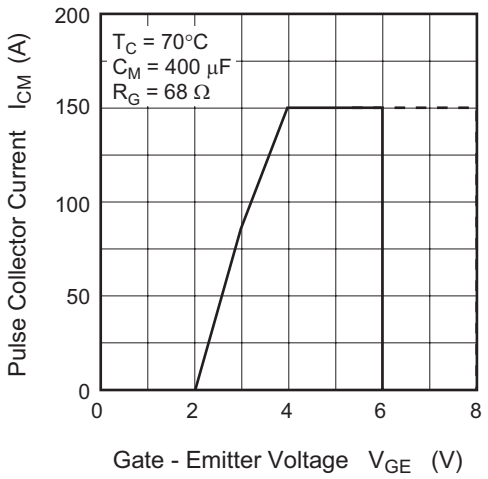
Electrical Characteristics

(Tj = 25°C)

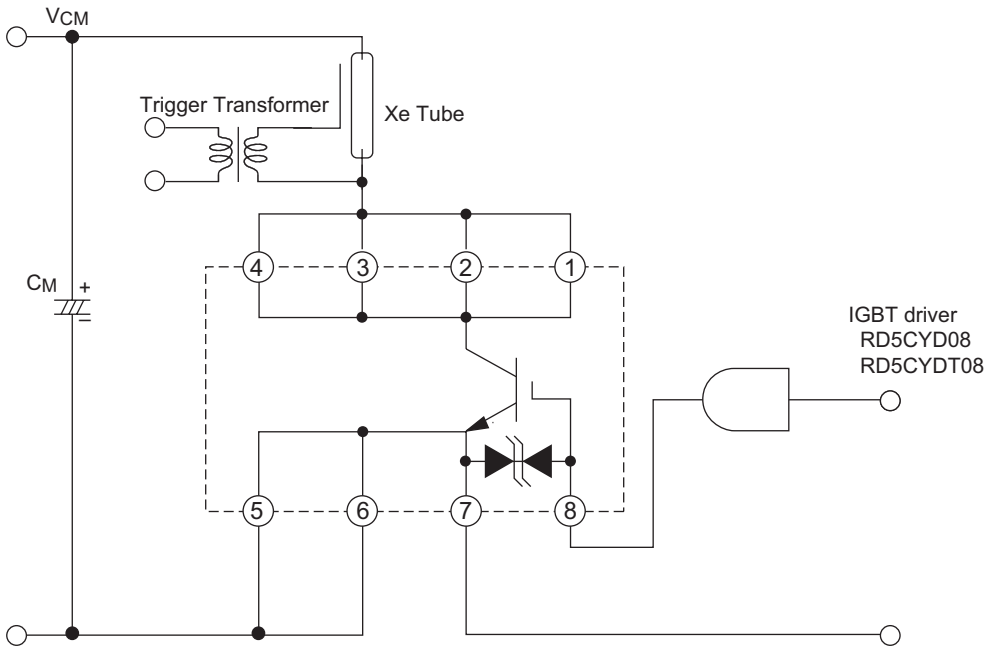
Parameter	Symbol	Min.	Typ.	Max.	Unit	Test conditions
Collector-emitter breakdown voltage	$V_{(BR)CES}$	450	—	—	V	I_C = 1 mA, V_{GE} = 0 V
Collector-emitter leakage current	I_{CES}	—	—	10	µA	V_{CE} = 400 V, V_{GE} = 0 V
Gate-emitter leakage current	I_{GES}	—	—	±10	µA	V_{GE} = ±6 V, V_{CE} = 0 V
Gate-emitter threshold voltage	$V_{GE(th)}$	0.5	0.7	1.5	V	V_{CE} = 10 V, I_C = 1 mA
Collector-emitter saturation voltage	$V_{CE(sat)}$	—	5.0	10.0	V	I_C = 150 A, V_{GE} = 4 V
Input capacitance	Cies	—	5000	—	pF	V_{CE} = 25 V, V_{GE} = 10 V, f = 1MHz

Performance Curves

Maximum Pulse Collector Current
(Conductive Capability in Strobe Flash Applications)



Application Example



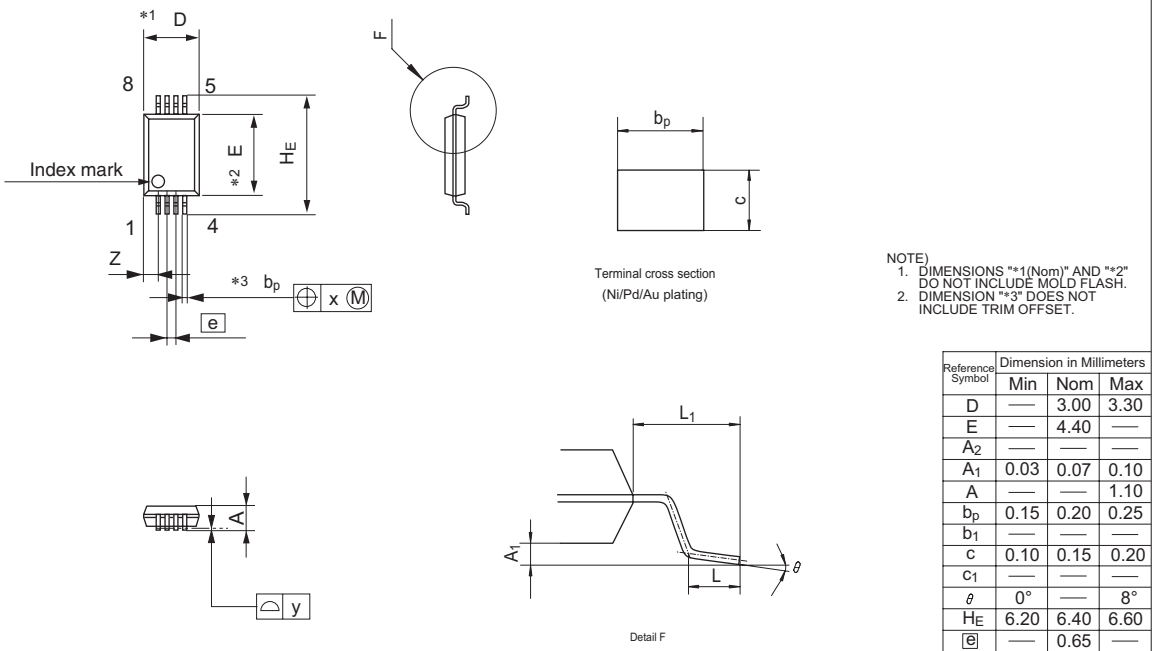
	Recommended Operation Conditions	Maximum Operation Conditions
V_{CM}	330 V	350 V
I_{CP}	130 A	150 A
C_M	330 μF	400 μF
V_{GE}	5 V	4 V

Precautions on Usage

1. IGBT has MOS structure and its gate is insulated by thin silicon oxide. So please handle carefully to protect the device from electrostatic charge.
2. Gate drive voltage during on-period must be applied to satisfy the rating of maximum pulse collector current. And turn-off dv/dt must become less than 400 V/ μs. In general, when $R_{G\ (off)} = 68\ \Omega$, it is satisfied.
3. For safety use, we recommend that the ground of the drive signal is connected to pin 7.
4. The operation life should be endured until repeated discharge of 5,000 times under the charge current ($I_{Xe} \leq 150$ A : full luminescence condition) of main capacitor ($C_M = 400\ \mu F$). Repetition period under full luminescence condition is over 3 seconds.
5. Total operation hours applied to the gate-emitter voltage must be within 5,000 hours when V_{GE} is driven at 6 V.

Package Dimensions

Package Name	JEITA Package Code	RENESAS Code	Previous Code	MASS[Typ.]
TSSOP-8	P-TSSOP8-4.4 × 3-0.65	PTSP0008JB-B	TTP-8DV	0.037g



NOTE)
1. DIMENSIONS "*1(Nom)" AND "*2" DO NOT INCLUDE MOLD FLASH.
2. DIMENSION "*3" DOES NOT INCLUDE TRIM OFFSET.

Reference Symbol	Dimension in Millimeters		
	Min	Nom	Max
D	—	3.00	3.30
E	—	4.40	—
A_2	—	—	—
A_1	0.03	0.07	0.10
A	—	—	1.10
b_p	0.15	0.20	0.25
b_1	—	—	—
c	0.10	0.15	0.20
c_1	—	—	—
θ	0°	—	8°
H_E	6.20	6.40	6.60
e	—	0.65	—
x	—	—	0.13
y	—	—	0.10
Z	—	—	0.805
L	0.40	0.50	0.60
L_1	—	1.0	—

Order Code

Lead form	Standard packing	Quantity	Standard order code	Standard order code example
Surface-mounted type	Taping	3000	Type name – 00 – Q0	RJP4003ASA-0-Q0

Note: Please confirm the specification about the shipping in detail.

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