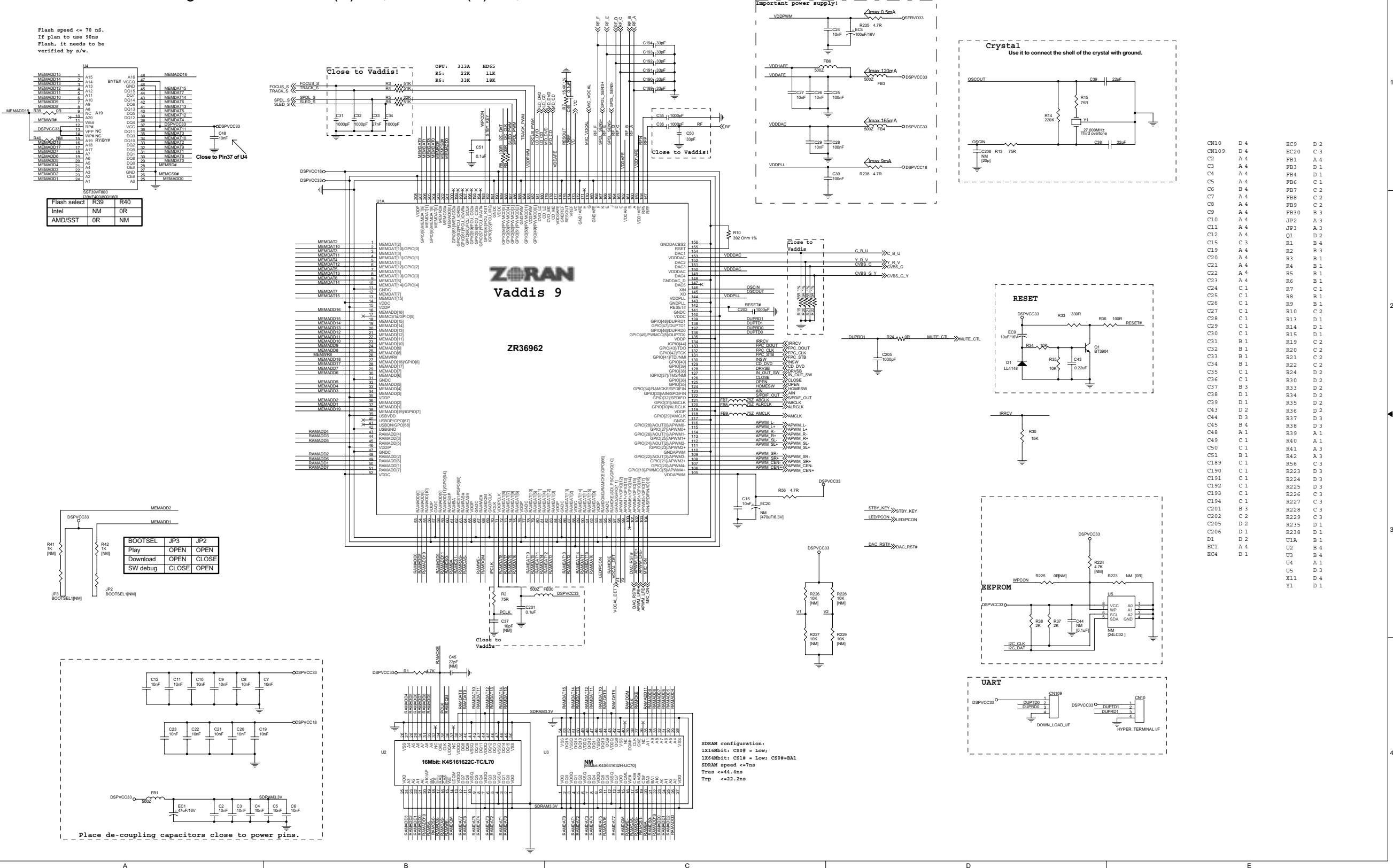
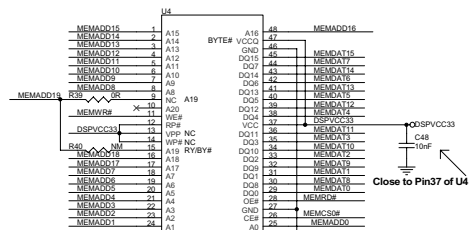


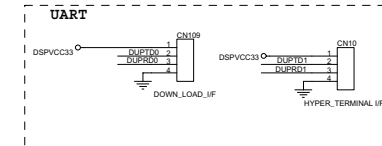
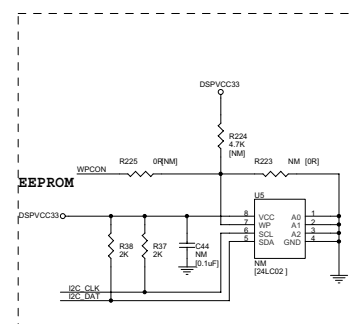
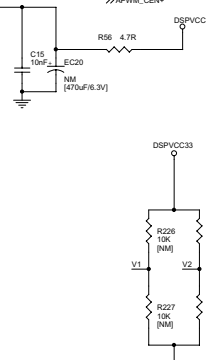
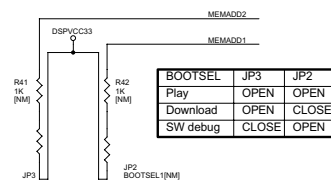
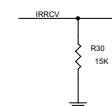
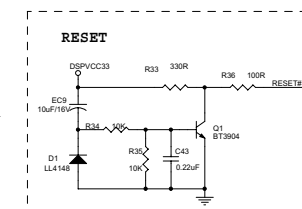
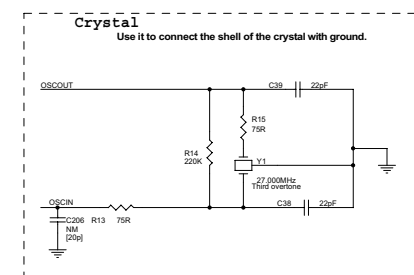
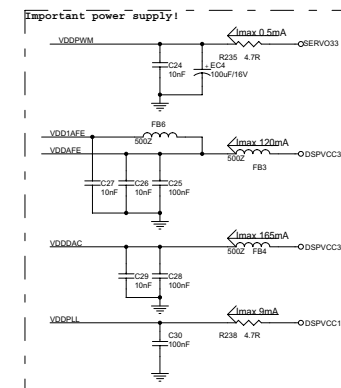
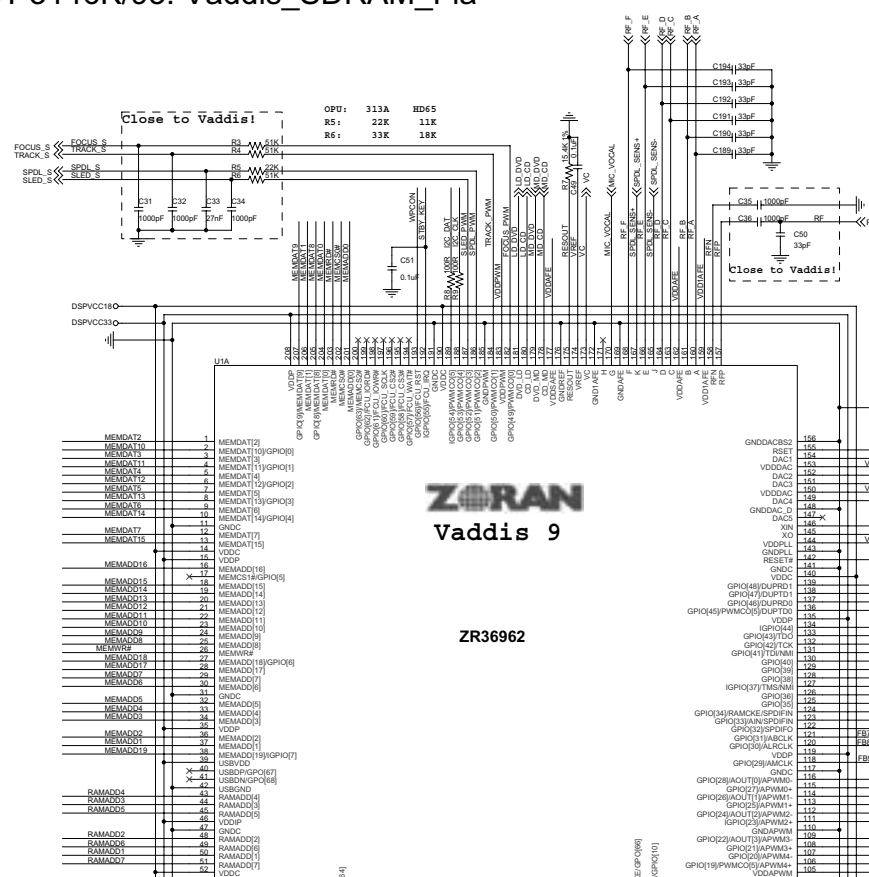
Main Board Electric Diagram for DVP3126(K)/XX, DVP3146(K)/XX, DVP3148K/XX: INDEX



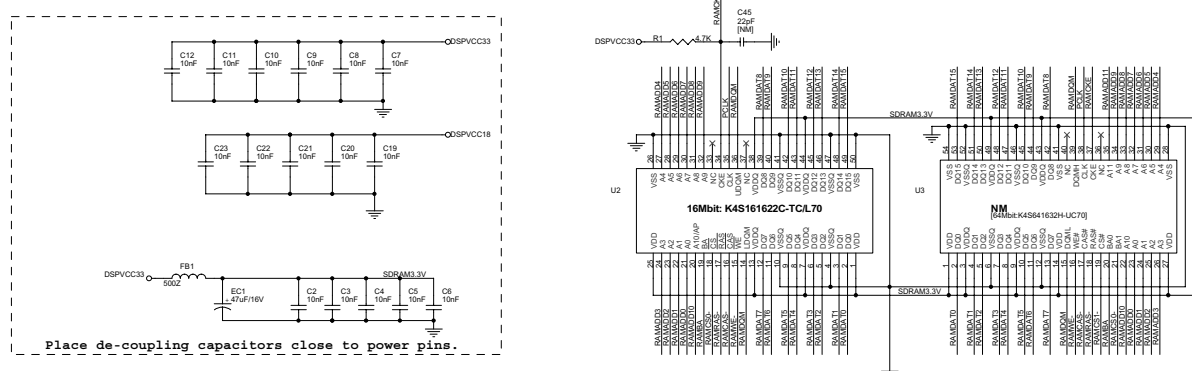
Flash speed <= 70 nS.
If plan to use 90ns
Flash, it needs to be
verified by s/w.



[39V/F400/800/1600]		
Flash select	R39	R40
Intel	NM	0R
AMD/SST	0R	NM



CN10	D 4	EC9	D 2
CN109	D 4	EC20	C 3
C2	A 4	FB1	A 4
C3	A 4	FB3	D 1
C4	A 4	FB4	D 1
C5	A 4	FB6	A 4
C6	B 4	FB7	C 2
C7	A 4	FB8	C 2
C8	A 4	FB9	C 2
C9	A 4	FB30	B 3
C10	A 4	JP2	A 4
C11	A 4	JP3	A 3
C12	A 4	Q1	D 2
C15	C 3	R1	B 4
C19	A 4	R2	B 3
C20	A 4	R3	A 1
C21	A 4	R4	B 1
C22	A 4	R5	B 1
C23	A 4	R6	B 1
C24	C 1	R7	C 1
C25	C 1	R8	B 1
C26	C 1	R9	B 1
C27	C 1	R10	C 2
C28	C 1	R13	D 1
C29	C 1	R14	D 1
C30	C 1	R15	D 1
C31	B 1	R19	C 2
C32	B 1	R20	C 2
C33	B 1	R21	C 2
C34	B 1	R22	C 2
C35	C 1	R24	C 2
C36	C 1	R30	D 2
C37	B 3	R33	D 2
C38	D 1	R34	D 2
C39	D 1	R35	D 2
C43	D 2	R36	D 2
C44	D 3	R37	D 2
C45	B 4	R38	D 3
C48	A 1	R39	A 1
C49	C 1	R40	A 1
C50	C 1	R41	A 3
C51	B 1	R42	A 3
C189	C 1	R56	C 3
C190	C 1	R223	D 3
C191	C 1	R224	D 3
C192	C 1	R225	D 3
C193	C 1	R226	C 3
C194	C 1	R227	C 3
C201	B 3	R228	C 3
C202	C 2	R229	C 3
C203	C 2	R230	D 1
C206	D 1	R238	D 1
D1	D 2	U1A	B 4
EC1	A 4	U2	B 4
EC4	D 1	U3	B 4
		U4	A 1
		U5	D 3
		X11	D 4
		Y1	D 4



```
SDRAM configuration:
1X16Mbit: CS0# = Low;
1X64Mbit: CS1# = Low; CS0# = BA
SDRAM speed <= 7ns
Tras <= 44.4ns
Trp <= 22.2ns
```